

# Contents

<b>Affidavit</b>	<b>ii</b>
<b>Digest / Propos</b>	<b>iii</b>
<b>Abstract</b>	<b>iv</b>
<b>Résumé</b>	<b>vi</b>
<b>Contents</b>	<b>x</b>
<b>1. Introduction en français</b>	<b>1</b>
1.1. Synthèse de programmes . . . . .	1
1.2. Les systèmes réactifs . . . . .	2
1.3. La synthèse réactive et le problème de Church . . . . .	6
1.4. Extension aux alphabets infinis . . . . .	9
1.5. Extension au cas asynchrone . . . . .	18
1.6. Plan . . . . .	21
1.7. Comment lire cette thèse ? . . . . .	22
<b>2. Introduction in English</b>	<b>24</b>
2.1. Program Synthesis . . . . .	24
2.2. Reactive Systems . . . . .	25
2.3. Reactive Synthesis and the Church Problem . . . . .	28
2.4. Extension to the Infinite Alphabet Case . . . . .	31
2.5. Further Extension to the Asynchronous Case . . . . .	38
2.6. Outline . . . . .	41
2.7. How to Read this Thesis? . . . . .	41
<b>I. REACTIVE SYNTHESIS</b>	<b>44</b>
<b>3. Reactive Synthesis over Finite Alphabets</b>	<b>45</b>
3.1. Relations and Functions . . . . .	46
3.2. Uniformisation and Program Synthesis . . . . .	46
3.3. The Reactive Synthesis Problem . . . . .	48
3.4. The Church Synthesis Problem . . . . .	52
3.5. Games for Reactive Synthesis . . . . .	65
<b>4. Register Automata over a Data Domain</b>	<b>72</b>
4.1. Data Words . . . . .	74
4.2. Tests and Valuations . . . . .	76
4.3. Register Automata . . . . .	77
4.4. General Properties . . . . .	84
4.5. Non-Deterministic Register Automata with Equality . . . . .	93
4.6. Non-Deterministic Register Automata with a Dense Order . . . . .	107
4.7. Register Automata with a Discrete Order . . . . .	110
4.8. Universal Register Automata . . . . .	120
4.9. Register Automata and Arithmetic . . . . .	123

4.10. Extensions of the Model . . . . .	124
<b>5. Synthesis Problems over Data Words</b>	<b>132</b>
5.1. Reactive Synthesis over Data Words . . . . .	132
5.2. Data Automatic Specifications . . . . .	133
5.3. Register Transducers . . . . .	134
5.4. The Church Synthesis Problem . . . . .	138
<b>6. Register-Bounded Synthesis of Register Transducers</b>	<b>139</b>
6.1. A Generic Approach . . . . .	140
6.2. Universal Register Automata Specifications . . . . .	150
6.3. Non-Deterministic Register Automata Specifications . . . . .	154
<b>7. Unbounded Synthesis of Register Transducers</b>	<b>162</b>
7.1. Non-Deterministic Register Automata Specifications . . . . .	163
7.2. Universal Register Automata Specifications . . . . .	163
7.3. Deterministic Register Automata Specifications . . . . .	165
<b>8. Partial Versus Total Domain</b>	<b>198</b>
8.1. Domains of Specification Automata . . . . .	198
8.2. Uniformisation Problem . . . . .	200
8.3. Good-Enough Synthesis . . . . .	200
<b>9. Conclusion</b>	<b>202</b>
<b>II. COMPUTABILITY OF FUNCTIONS DEFINED BY TRANSDUCERS OVER INFINITE ALPHABETS</b>	<b>205</b>
<b>10. Non-Deterministic Asynchronous Register Transducers</b>	<b>208</b>
10.1. The Model . . . . .	208
10.2. Register Transducers and Register Automata . . . . .	212
10.3. Functions Recognised by Transducers . . . . .	215
10.4. Closure under Composition . . . . .	217
<b>11. <math>\omega</math>-Computability and Continuity</b>	<b>219</b>
11.1. Computability . . . . .	219
11.2. Continuity . . . . .	222
11.3. $\omega$ -Computability versus Continuity . . . . .	225
<b>12. Exploration of Various Data Domains</b>	<b>229</b>
12.1. Data Domains with Equality . . . . .	229
12.2. Oligomorphic Data Domains . . . . .	243
12.3. Discretely Ordered Data Domains . . . . .	273
<b>13. Conclusion</b>	<b>310</b>
<b>14. Thesis Conclusion</b>	<b>313</b>
14.1. Abstraction of the Behaviour of Register Automata . . . . .	313
14.2. Generalisation to Other Formalisms . . . . .	314
14.3. Minimisation and Learning . . . . .	315

<b>APPENDIX</b>	<b>317</b>
<b>A. Graphical Conventions</b>	<b>318</b>
A.1. Relations and Functions, Input and Output . . . . .	318
A.2. Reactive Synthesis . . . . .	318
A.3. $\omega$ -Automata . . . . .	318
A.4. Register Automata . . . . .	319
A.5. Synchronous Sequential Register Transducers . . . . .	319
A.6. Non-Deterministic Asynchronous Register Transducers . . . . .	319
<b>B. Details of Chapter 3</b>	<b>320</b>
B.1. Section 3.4.1 (Logics for Specifications) . . . . .	320
B.2. Section 3.4.3 (Automata) . . . . .	321
<b>C. Details of Part II</b>	<b>324</b>
C.1. Section 12.3.1 (Register Automata over a Discrete Domain) . . . . .	324
<b>Bibliography</b>	<b>326</b>