

Dépôt Institutionnel de l'Université libre de Bruxelles / Université libre de Bruxelles Institutional Repository Thèse de doctorat/ PhD Thesis

#### **Citation APA:**

Seiedrazi, H. (1969). The concept and design of incremental computers (Unpublished doctoral dissertation). Université libre de Bruxelles, Faculté des sciences. Bruxelles.

Disponible à / Available at permalink : https://dipot.ulb.ac.be/dspace/bitstream/2013/215105/4/45314670-bf95-4b6e-8b07-fc646e0bb939.txt

#### (English version below)

Cette thèse de doctorat a été numérisée par l'Université libre de Bruxelles. L'auteur qui s'opposerait à sa mise en ligne dans DI-fusion est invité à

prendre contact avec l'Université (di-fusion@ulb.be).

#### Dans le cas où une version électronique native de la thèse existe, l'Université ne peut garantir que la présente version numérisée soit identique à la version électronique native, ni qu'elle soit la version officielle définitive de la thèse.

DI-fusion, le Dépôt Institutionnel de l'Université libre de Bruxelles, recueille la production scientifique de l'Université, mise à disposition en libre accès autant que possible. Les œuvres accessibles dans DI-fusion sont protégées par la législation belge relative aux droits d'auteur et aux droits voisins. Toute personne peut, sans avoir à demander l'autorisation de l'auteur ou de l'ayant-droit, à des fins d'usage privé ou à des fins d'illustration de l'enseignement ou de recherche scientifique, dans la mesure justifiée par le but non lucratif poursuivi, lire, télécharger ou reproduire sur papier ou sur tout autre support, les articles ou des fragments d'autres œuvres, disponibles dans DI-fusion, pour autant que : - Le nom des auteurs, le titre et la référence bibliographique complète soient cités;

- L'identifiant unique attribué aux métadonnées dans DI-fusion (permalink) soit indiqué;

- Le contenu ne soit pas modifié.

L'œuvre ne peut être stockée dans une autre base de données dans le but d'y donner accès ; l'identifiant unique (permalink) indiqué ci-dessus doit toujours être utilisé pour donner accès à l'œuvre. Toute autre utilisation non mentionnée ci-dessus nécessite l'autorisation de l'auteur de l'œuvre ou de l'ayant droit.

------ English Version -----

This Ph.D. thesis has been digitized by Université libre de Bruxelles. The author who would disagree on its online availability in DI-fusion is

invited to contact the University (di-fusion@ulb.be).

If a native electronic version of the thesis exists, the University can guarantee neither that the present digitized version is identical to the native electronic version, nor that it is the definitive official version of the thesis.

DI-fusion is the Institutional Repository of Université libre de Bruxelles; it collects the research output of the University, available on open access as much as possible. The works included in DI-fusion are protected by the Belgian legislation relating to authors' rights and neighbouring rights. Any user may, without prior permission from the authors or copyright owners, for private usage or for educational or scientific research purposes, to the extent justified by the non-profit activity, read, download or reproduce on paper or on any other media, the articles or fragments of other works, available in DI-fusion, provided:

- The authors, title and full bibliographic details are credited in any copy;

- The unique identifier (permalink) for the original metadata page in DI-fusion is indicated;
- The content is not changed in any way.

It is not permitted to store the work in another database in order to provide access to it; the unique identifier (permalink) indicated above must always be used to provide access to the work. Any other use not mentioned above requires the authors' or copyright owners' permission. D 934/a

### UNIVERSITE LIBRE DE BRUXELLES Faculté des Sciences Appliquées

Laboratoires d'Electronique industrielle et d'Automatique

## THE CONCEPT AND DESIGN OF INCREMENTAL COMPUTERS

### ALGORITHMS AND DESIGN

Thèse de doctorat



Hassan SEIEDRAZI Avril 1969

man have been a second

### UNIVERSITE LIBRE DE BRUXELLES Faculté des Sciences Appliquées Laboratoires d'Electronique industrielle et d'Automatique

1

all consider and and mo

NOA

BIBLIOTHÈQUE DE MATHÉMATIQUES ET DE PHYSIQUE

### THE CONCEPT AND DESIGN OF INCREMENTAL COMPUTERS

ALGORITHMS AND DESIGN

Thèse de doctorat



Hassan SEIEDRAZI Avril 1969

BMP

Se 42

v. 2

681.32

#### CHAPTER V

#### THE TOTAL ERROR AND THE CHOICE OF ALGORITHMS FOR INCREMENTAL COMPUTERS

5.1. Total error in the integration process in unitary or multiple increment computers, when the independent variable of integration X, is the independent variable t.

As we have seen in the preceeding chapters, the actual value of integral s (t) in incremental computer is replaced by the approximated interpolated quantized rounded off and delayed value of integral  $\Gamma_{QMD(k)}^{*}$  (t), which cause the error of method  $\Gamma(t)$ , the error of quantization  $\epsilon_{tQ}$ , the round off error  $e_k$  and the error of transmission  $\epsilon_{Tr}$ . The relation between these values is:

$$s_{(k)}(t) = s_{QMD(k)}^{*}(t) + [\Gamma(t) + \epsilon_{tQ}(t) + e_{k}(t) + \epsilon_{Tr}]$$
$$t \in (t_{0}, t_{k}) \qquad (5-1)$$

417086 The total error of process of integration  $\varepsilon_t$  is equal to:

$$\varepsilon_{t} = \Gamma(t) + \varepsilon_{tQ} (t) + e_{k} (t) + \varepsilon_{Tr}$$

$$t \equiv (t_{o}, t_{k})$$
(5-2)

so the equation (5-1) can be written as:

$$s_{(k)} (t) = \int_{QMD(k)}^{\infty} (t) + \varepsilon_t (t) \qquad t \in (t_0, t_k) \quad (5-3)$$

where  $s_k$  (t) is the actual value of integration in interval  $t \equiv (t_0, t_k)$ ,  $s_{QMD(k)}^{\times}$  (t) is the approximated interpolated, quantized, rounded off and delayed value of integration which is the algorithm of the incremental machine, and  $\epsilon_t$  (t) is the total error of process of integration.

Now, we will calculate the total error of process of integration for the rectangular, trapezoidal, and three points method of integration with unitary or multiple increments.

As it is shown in the preceeding chapters, in the rectangular method of integration with unitary increments, the error of method is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{1}{2} (\Delta x^{2} \cdot y' (x)) \qquad x \in (x_{1}, x_{1+1}) \quad (5-4)$$

$$\Gamma(t) = \frac{1}{2} \frac{(x_{k} - x_{0})^{2}}{k} y'(\xi) \quad \xi \in (x_{0}, x_{k}) \quad (5-5)$$

the error of quantization is:

$$\varepsilon_{tQ} < \Delta y (x_{kQ} - x_{oQ})$$
 (5-6)

The round off error is:

$$e_k < \Delta x$$
 (5-7)

So the total error of process of integration in rectangular method of integration with unitary increments, can be found from equations (5-2), (5-4), (5-5), (5-6) and (5-7) as following:

$$\varepsilon_{t} < \frac{1}{2} \frac{(\mathbf{x}_{k} - \mathbf{x}_{0})^{2}}{k} \mathbf{y}'(\xi) + \Delta \mathbf{y} (\mathbf{x}_{kQ} - \mathbf{x}_{0Q}) + \Delta \mathbf{x} + \varepsilon_{Tr}^{(5-8)}$$

The approximated interpolated, quantized, rounded off value of integral  $s_{QM(k)}^{x}$  (t), using the rectangular method of integration, which is the algorithm of incremental machine with unitary increments is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} y_{iQ} \cdot \Delta_{iQ}^{*}$$
 (5-9)

The error of method in rectangular method of integration with multiple increments ( $\delta x = 2^r \cdot \Delta x$ ,  $\delta y = 2^r \cdot \Delta y$ , and  $\delta s = 2^r \cdot \Delta s$ ) is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{1}{2} (\delta x)^{2} y'(x)$$

$$t \in (t_{i}, t_{i+1})$$
(5-10)

The error of quantization is:

$$\varepsilon_{tQ} < \Delta y \cdot 2^h (x_{kQ} - x_{oQ})$$
 (5-11)

The round off error is:

$$e_k < \delta x$$
 (5-12)

so the total error of process of integration in rectangular method of integration with multiple increments can be found from equations (5-2) (5-10), (5-11) and (5-12) as following:

$$\varepsilon_{t} < \left[\sum_{i=1}^{k} \frac{1}{2} (\delta x)^{2} y'(x) + \Delta y \cdot 2^{h} (x_{kQ} - x_{oQ}) + \delta x + \varepsilon_{Tr}\right]$$
(5-13)

and the approximated interpolated quantized, rounded off value of integral  $s_{QM(k)}^{\times}$  (t), using the rectangular method of integration, which is the algorithm of incremental computer with multiple increments is:

$$\mathbf{s}_{QM(k)}^{\mathsf{x}}(t) = \sum_{i=1}^{k} \mathbf{y}_{iQ} \cdot \delta_{iQ}^{\mathsf{x}}$$
(5-14)

In the trapezoidal method of integration with unitary increments, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{1}{12} (\Delta x)^{3} y''(x) \qquad x \in (x_{i}, x_{i+1})$$
(5-15)

$$= \frac{1}{12} \frac{(x_{k} - x_{0})^{3}}{k^{2}} y^{*} (\xi) \qquad \xi \in (x_{0}, x_{k})$$

The quantization error  $\epsilon_{tO}$  (t) is:

$$\epsilon_{\pm 0}$$
 (t) <  $\Delta y (x_{k0} - x_{00})$  (5-16)

The round off error ek (t) is:

$$e_{\rm k}$$
 (t) <  $\Delta x$  (5-17)

Therefore from equations (5-2), (5-15), (5-16) and (5-17), the total error of the process of integration in incremental machine with unitary increments is:

$$\varepsilon_{t} < \frac{1}{12} \frac{(\mathbf{x}_{kQ} - \mathbf{x}_{oQ})^{3}}{k^{2}} \mathbf{y}^{*} (\xi) + \Delta \mathbf{y} (\mathbf{x}_{kQ} - \mathbf{x}_{oQ}) + \\ + \Delta \mathbf{x} + \varepsilon_{mr}$$
(5-18)

The approximated interpolated, rounded off value of integration,  $s_{QM}^{\kappa}$  (t), using the trapezoidal method of integration, which is the algorithm of the incremental computer with unitary increments is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} (y_{iQ} \circ \Delta_{iQ}^{*} + \frac{1}{2} \Delta_{iQ}^{*} \circ \Delta_{iQ}^{*})$$
 (5-19)

In the trapezoidal method of integration, with multiple increments, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{1}{12} (\delta x)^{3} y^{*}(x) \qquad x \in (x_{i}, x_{i+1}) \quad (5-20)$$

The quantization error  $\epsilon_{tO}$  (t) is:

$$\epsilon_{tQ}$$
 (t) <  $\Delta y \cdot 2^{h} (x_{kQ} - x_{oQ})$  (5-21)

The round off error  $e_k$  (t) is:

$$e_k$$
 (t) <  $\delta x$  (5-22)

Therefore from equations (5-2), (5-20), (5-21) and (5-22), the total error of process of integration  $\epsilon_t$  in incremental computer with multiple increments is:

$$\varepsilon_{t} < \sum_{i=1}^{k} \frac{1}{12} (\delta x)^{3} y'' (x) + \Delta y \circ 2^{h} (x_{kQ} - x_{oQ}) + \delta x + \varepsilon_{Tr}$$

$$(5-23)$$

and the approximated interpolated, rounded off value of integration  $s_{QM(k)}^{*}$  (t) which is the algorithm of the incremental machine using the trapezoidal method of integration with multiple increments is:

$$s_{QM(k)}^{x}(t) = \sum_{i=1}^{k} (y_{iQ} \cdot \delta_{iQ}x + \delta_{iQ}y \cdot \delta_{iQ}x)$$
 (5-24)

In the three points method of integration, with unitary increments, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{1}{24} (\Delta x^{i} y^{\prime \prime \prime} (x)) \qquad x \in (x_{1}, x_{i+1})$$

$$= \frac{1}{24} \frac{(x_{k} - x_{0})^{4}}{k^{3}} y^{\prime \prime \prime} (\xi) \qquad \xi \in (x_{0}, x_{k})$$
(5-25)

The quantization error  $\epsilon_{tO}$  is:

$$\varepsilon_{tQ} < \Delta y (x_{kQ} - x_{oQ})$$
 (5-26)

The round off error  $e_k$  is:

$$e_k < \Delta x$$
 (5-27)

Therefore, from equations (5-2), (5-25), (5-26) and (5-27), the total error  $\varepsilon_t$  of process of integration with unitary increments is:

$$\varepsilon_{t} < \frac{1}{24} \frac{(x_{k} - x_{o})^{4}}{k^{3}} y'''(\xi) + \Delta y (x_{kQ} - x_{oQ}) + \Delta x + \varepsilon_{Tr}$$

The approximated interpolated, quantized and rounded off value of integration  $s_{QM(k)}^{\times}$  (t), using the three points method of integration which is the algorithm of incremental computer, with unitary increments is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} [y_{iQ} \circ A_{iQ}x + \frac{1}{2}A_{iQ}x \circ A_{iQ}y + \frac{1}{12}A_{iQ}x (A_{iQ}y - A_{iQ}x)]$$

In the three points method of integration, with multiple increments, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{1}{24} (\delta x)' y'''(x) \qquad x \in (x_i, x_{i+1}) \quad (5-30)$$

The quantization error  $\varepsilon_{tO}$  is:

$$\epsilon_{to} < \Delta y \cdot 2^{n} (x_{ko} - x_{oQ})$$
 (5-31)

The round off error e, is:

$$e_k < \delta x$$
 (5-32)

Therefore, from equations (5-2), (5-30), (5-31) and (5-32), the total error  $\epsilon_t$  of process of integration, with multiple increments is:

$$\varepsilon_{t} < \left[\sum_{i=1}^{k} \frac{1}{24} (\delta x)^{4} y'''(x) + \Delta y \cdot 2^{h} (x_{kQ} - x_{oQ}) + \delta x + \delta x + \varepsilon_{Tr}\right]$$

$$(5-33)$$

The approximated, interpolated, quantized and rounded off value of integration  $s_{QM(k)}^{\times}$  (t), using the three points method of integration, which is the algorithm of incremental machine with multiple increments is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} [y_{iQ} \circ \delta_{iQ}x + \frac{1}{2} \delta_{iQ}x \circ \delta_{iQ}y + \frac{1}{$$

$$\frac{1}{12} \delta_{1Q} \times (\delta_{1Q} Y - \delta_{(1-1)Q} Y) ] \qquad (5-34)$$

The block diagram of incremental computers is drawn in figure (5.1).

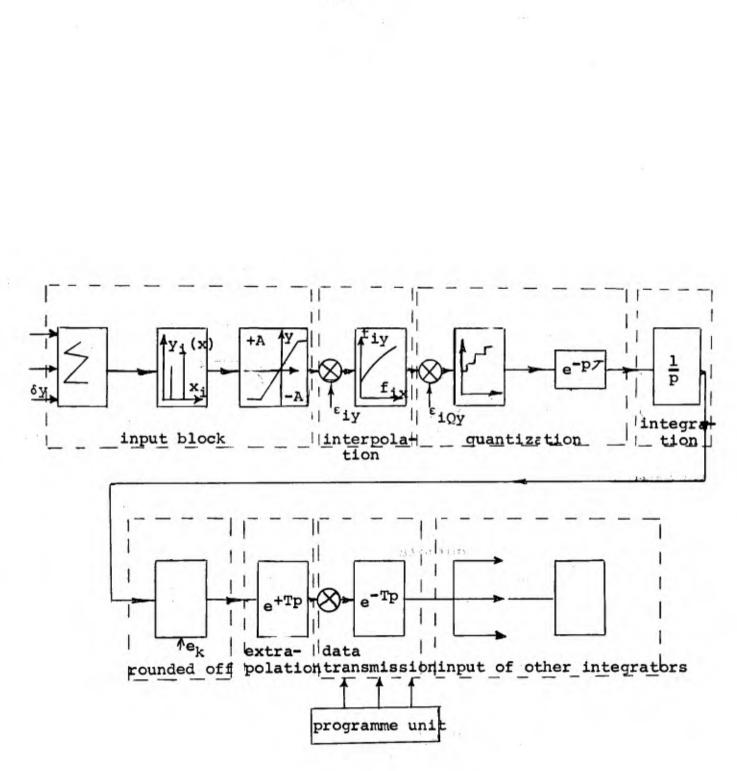


fig. 5.1.

Block diagram of incremental computer.

5.2. Total error in the integration process in unitary or multiple increment computers, when the independent variable of integration X is a function of the independent variable t.

As it was discussed earlier, the actual value of integral s (t) is replaced with the approximated, interpolated, quantized rounded off and delayed value of integral  $s_{OMD(k)}^{x}$  (t) as:

$$s (t) = s_{QMD(k)}^{x} (t) + [r(t) + \varepsilon_{tQ}(t) + e_{k}(t) + \varepsilon_{Tr}]$$

$$(5-35)$$

$$s (t) = s_{OMD(k)}^{x} (t) + \varepsilon_{t}$$

$$\varepsilon_{t} = \Gamma(t) + \varepsilon_{t0} (t) + \varepsilon_{k} (t) + \varepsilon_{Tr}$$
(5-36)

where  $\varepsilon_t$  is the total error of process of integration, which is equal to the sum of the error of method  $\Gamma(t)$ , the error of quantization  $\varepsilon_{tO}$  (t), the round off error  $e_{t}$  (t) and the transmission error  $\varepsilon_{Tr}$ .

As we have seen in the preceeding chapter, the error of the rectangular method of integration  $\Gamma(t)$ , with unitary increments, when the variable of integration X is a function of the independent variable to f machine:

$$\Gamma(t) = \frac{1}{2} \sum_{i=1}^{k} (\Delta t)^{2} y^{i}(t) x^{i}(t) \qquad t \in (t_{i}, t_{i+1})$$

$$= \frac{1}{2} \frac{(t_{k} - t_{0})^{2}}{k} y^{i}(\xi) x^{i}(\xi) \qquad \xi \subseteq (x_{k}, x_{0})$$
(5-37)

The quantization error  $\epsilon_{\pm 0}$  is:

$$\epsilon_{tQ} < \Delta y (x_{Qk} - x_k) + \Delta x (y_{Qk} - y_{ok})$$
 (5-38)

The round off error ek is:

$$e_k < \Delta x$$
 (5-39)

and emr depends on the programming.

+ [

So the total error  $\varepsilon_t$  of integration process in rectangular method, with unitary increment, when the independent variable of integral is a function of the independent variable t of machine, can be found by the equations (5-36), (5-37), (5-38) and (5-39) as:

$$\varepsilon_{t} < \left[ \frac{1}{2} \frac{(t_{k} - t_{o})^{2}}{k} y'(\xi) \cdot x'(\xi) + \frac{1}{k} (y_{ok} - y_{oQ}) + \Delta x (y_{ok} - y_{oQ}) + \Delta x + \varepsilon_{Tr} \right]$$

$$(5-40)$$

and the approximated interpolated, quantized, rounded off value of integration  $s_{OM(k)}^{*}$  (t), using the unitary increments is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} y_{iQ} \cdot \Delta_{iQ}^{*}$$
 (5-41)

In the rectangular method of integration with multiple increments, when the independent variable of integral X is a function of the independent variable to f machine, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \frac{1}{2} \sum_{i=1}^{k} (\delta x)^{2} y'(t) x'(t) \qquad t \in (t_{i}, t_{i+1})$$
(5-42)

The quantization error  $\varepsilon_{\pm 0}$  is:

$$\varepsilon_{tQ} < 2^{h} \cdot \Delta y (x_{Qk} - x_{ok}) + \Delta x y_{Qk} - y_{oQ})$$
 (5-43)  
The round off error  $e_k$  is:

$$e_k < \delta x$$
 (5-44)

and the transmission error  $\boldsymbol{\epsilon}_{\boldsymbol{\pi}_{\mathbf{r}}}$  depends on the programming.

The total error  $\epsilon_t$  of the rectangular method of integration, with multiple increments when the independent variable of integral X is a function of the independent variable t of machine, can be find from equations (5-36), (5-42), (5-43) and (5-44) as following:

$$\varepsilon_{t} < \left| \frac{1}{2} \sum_{i=1}^{k} [(\delta t) y'(t) x'(t)] + [2^{h} \cdot \Delta y (x_{Qk} - x_{oQ}) + \Delta x (y_{Qk} - y_{oQ})] + \delta x + \varepsilon_{Tr} \right| (5-45)$$

and the approximated interpolated, quantized, and rounded off value of integration  $s_{QM(k)}^{*}$  (t), with multiple increments ( $\delta x = 2^{r} \cdot \Delta x$ ,  $\delta y = 2^{r} \cdot \Delta y$  and  $\delta s = 2^{r} \cdot \Delta s$ ) is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} y_{iQ} \cdot \delta_{iQ}^{*}$$
 (5-46)

In the trapezoidal method of integration, with unitary increments, when the independent variable of integral X is a function of the independent variable to f machine, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \sum_{i=1}^{k} (\Delta t)^{3} [y''(t) x'(t) - y'(t) x''(t)] \qquad (5-47)$$

$$x \in (x_{i}, x_{i+1})$$

$$= \frac{(t_{k} - t_{0})^{3}}{t^{2}} [y''(\xi) x'(\xi) - y'(\xi) x''(\xi)], \xi \in (x_{0}, x_{k})$$

The quantization error  $\epsilon_{\pm 0}$  is:

$$\epsilon_{\pm 0} < \Delta y (x_{0k} - x_{00}) + \Delta x (y_{0k} - y_{00})$$
 (5-48)

The round off error ek is:

$$e_k < \Delta x$$
 (5-49)

and the transmission error  $\varepsilon_{Tr}$  depends on the programming.

So, the total error  $\epsilon_t$  of trapezoidal method of integration, with unitary increments, when the independent variable of integral X is a function of the independent variable t of machine, can be found from equations (5-36), (5-47); (5-48) and (5-49) as following:

$$t < \sum_{i=1}^{k} \frac{(\Delta t)^{3}}{12} [y''(t) x'(t) - y'(t) x''(t)] + (5-50)$$

+ 
$$[\Delta y (x_{Ok} - x_{OO}) + \Delta x (y_{Ok} - y_{OO})] + \Delta x + \epsilon_{Tr}$$

The approximated interpolated, quantized and rounded off value of integration  $s_{OM(k)}^{\times}$  (t) with unitary increments is:

$$s_{QM(k)}^{\kappa}(t) = \sum_{i=1}^{k} (y_{iQ} \cdot \Delta_{iQ}x + \frac{1}{2} \Delta_{iQ}y \cdot \Delta_{iQ}x)$$
 (5-51)

The error of method  $\Gamma(t)$  in the trapezoidal method of integration, using the multiple increments, when the independent variable of integral X is a function of the independent variable t of machine, is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{(\delta t)^{3}}{12} [y''(t) x'(t) - y'(t) x''(t)] \qquad (5-52)$$

The quantization error  $\epsilon_{\pm 0}$  is:

$$\varepsilon_{tQ} < 2^{h} \cdot \Delta y (x_{Qk} - x_{oQ}) + \Delta x (y_{Qk} - y_{oQ})$$
 (5-53)

The round off error e, is:

$$e_k < \delta x$$
 (5-54)

and the transmission error err depends on the programming.

So, the total error  $\varepsilon_t$  in the trapezoidal method of integration, using the multiple increments, when the independent variable of integral is a function of the independent variable t of machine, can be found from equations (5-36), (5-52), (5-53) and (5-54) as:

$$\epsilon_{t} < \left[ \sum_{i=1}^{k} \frac{(\delta t)^{3}}{12} [y''(t) x'(t) - y'(t) x''(t)] + (5-55) \right]$$

$$+ [2^{h} \cdot \Delta y ' x_{Qk} - x_{Q}Q) + \Delta x (y_{Qk} - y_{Q}Q)] + \delta x + \epsilon_{Tr}$$

The algorithms of machine, which is the approximated interpolated, quantized and rounded off value of integration  $s_{QM(k)}^{\times}$  (t), using the multiple increments, when the independent variable of integral X is a function of the independent variable t of machine, is:

$$s_{QM(k)}^{*}(t) = \sum_{i=1}^{k} (y_{iQ} \cdot \delta_{iQ}x + \frac{1}{2} \delta_{iQ}x \cdot \delta_{iQ}y) \qquad x \in (x_{i}, x_{i+1})$$

In the three points method of integration, using the unitary increment, when the independent variable of integral X is a function of the independent variable t of machine, the error of method r(t) is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{(\Delta t)^{4}}{24} [y'''(t) x'(t) - y'(t) x'''(t)]$$
  

$$t \Xi(t_{i}, t_{i+1})$$
  

$$= \frac{(t_{k} - t_{0})^{4}}{24k^{3}} [y'''(\xi) x'(\xi) - y'(\xi) x'''(\xi)]$$
  

$$\xi \Xi(t_{0}, t_{k})$$
  
(5-57)

The quantization error  $\varepsilon_{t0}$  is:

$$\epsilon_{tO} < \Delta y (x_{Qk} - x_{oO}) + \Delta x (y_{Qk} - y_{OO})$$
 (5-58)

The round off error ek is:

$$e_k < \Delta x$$
 (5-59)

and the transmission error  $\varepsilon_{Tr}$  depends on the programming.

So the total error  $\varepsilon_t$ , in the three points method of integration, using the unitary increments, when the independent variable of integral X is a function of the independent variable of machine t, can be found from equations (5-36), (5-57), (5-58) and (5-59) as following:

$$\varepsilon_{t} < \left| \frac{\left[ (t_{k} - t_{0})^{4} \right]^{2}}{24k^{3}} \left[ y'''(t) x'(t) - y'(t) x'''(t) \right] + \frac{1}{24k^{3}} \right|$$

$$\left[ \Delta y (x_{Qk} - x_{0Q}) + \Delta x (y_{Qk} - y_{Q0}) \right] + \Delta x + \varepsilon_{Tr} \right|$$
(5-60)

The algorithm of machine, which is the approximated, interpolated, quantized and rounded off value of integration  $s_{OM(k)}^{x}$  (t), using the unitary increments, when the independent variable of integral X is a function of the independent variable t of machine, is:

'1Q'

12

$$s_{QM(k)}^{x}(t) = \sum_{i=1}^{k} [y_{iQ} \cdot \Delta_{iQ}x + \frac{1}{2}\Delta_{iQ}x \cdot \Delta_{iQ}y \cdot \Delta_{iQ}y + \frac{1}{2}\Delta_{iQ}x \cdot \Delta_{iQ}y \cdot \Delta_{iQ}y + \frac{1}{2}\Delta_{iQ}x \cdot \Delta_{iQ}y \cdot \Delta_{iQ}y \cdot \Delta_{iQ}y + \frac{1}{2}\Delta_{iQ}x \cdot \Delta_{iQ}y \cdot \Delta_$$

In the three points method of integration, using the multiple increments, when the independent variable of integral X is a function of the independent variable toof machine, the error of method  $\Gamma(t)$  is:

$$\Gamma(t) = \sum_{i=1}^{k} \frac{(\delta t)^{4}}{24} [y'''(t) x'(t) - y'(t) x'''(t)]$$
  
$$t \in (t_{i}, t_{i+1})$$
(5-62)

The quantization error  $\epsilon_{+0}$  is:

$$x_{tQ} < 2^{h} \cdot \Delta y (x_{kQ} - x_{oQ}) + \Delta x (x_{kQ} - x_{oQ})$$
 (5-63)

The round off error ek is:

$$\theta_k < \delta x$$
 (5-64)

and the transmission error try depends on the programming.

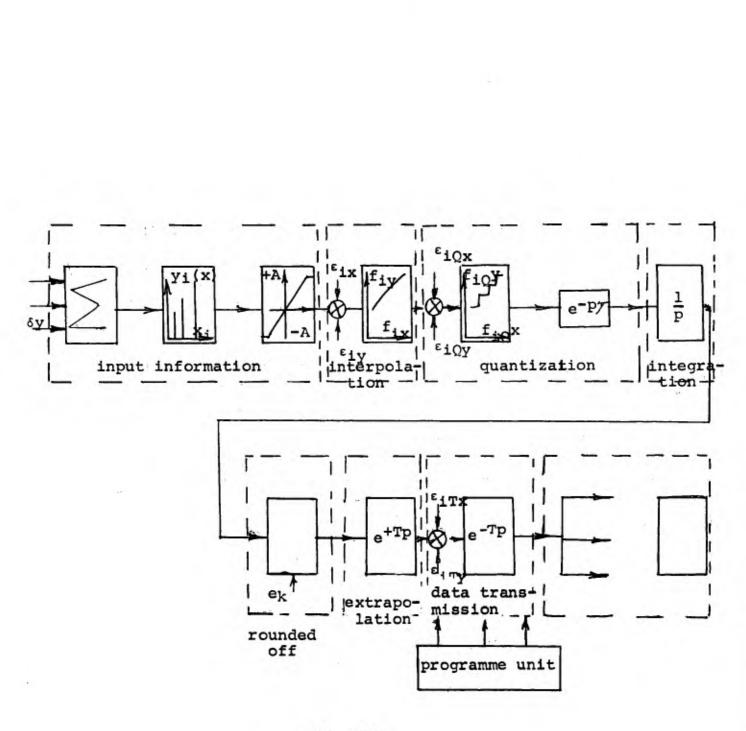
So the total error  $\epsilon_t$ , in the three points method of integration using the multiple increments, when the independent variable of integral X is a function of the independent variable t of machine, can be found from equations (5-36); (5-62), (5-63) and (5-64) as:

$$\varepsilon_{t} < \left| \frac{\sum_{i=1}^{k} \frac{(\delta t)^{4}}{24}}{24} [y^{*} (t) x'(t) - y'(t) x'''(t)] + \frac{1}{24} \frac{(\delta t)^{4}}{24} [y^{*} (t) x'(t) - y'(t) x'''(t)] + \frac{1}{24} \frac{(\delta t)^{4}}{24} [y^{*} (t) x'(t) - y'(t) x'''(t)] + \frac{1}{24} \frac{(\delta t)^{4}}{24} \frac{(\delta t)^{4}}{24} [y^{*} (t) x'(t) - y'(t) x'''(t)] + \frac{1}{24} \frac{(\delta t)^{4}}{24} \frac{(\delta t)^{4}}{24} [y^{*} (t) x'(t) - y'(t) x'''(t)] + \frac{1}{24} \frac{(\delta t)^{4}}{24} \frac{(\delta t)^{4}}{24} [y^{*} (t) x'(t) - y'(t) x'''(t)] + \frac{1}{24} \frac{(\delta t)^{4}}{24} \frac{(\delta t)^{4$$

The algorithms of machine, using the multiple increments, which are the approximated interpolated, quantized and rounded off value of integration  $s_{QM(k)}^{*}$  (t) are:

$$s_{QM(k)}^{X}(t) = \sum_{i=1}^{k} [y_{iQ} \circ \delta_{iQ}x + \frac{1}{2} \delta_{iQ}x \circ \delta_{iQ}y + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}y \circ \delta_{iQ}y + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}y \circ \delta_{iQ}y + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}y + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}y \circ \delta_{iQ}y + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}y \circ \delta_{iQ}y + \frac{1}{2} \delta_{iQ}y \circ \delta$$

The block diagram of the incremental computer is shown in figure (5.2).



#### fig. 5.2.

Block diagram of incremental computer.

5.3. Relative error in the integration process in unitary or multiple increment computers.

We calculated the total error  $\varepsilon_t$  of integration process in each method of integration. But the most useful formula for evaluating the error in a interval  $t \in (t_0, t_k)$  is the relative error Y, which is the ratio of the total absolute error  $\varepsilon_t$ , to the value of integration s (t) as:

$$\gamma = \frac{\varepsilon_{\rm t}}{\rm s \ (t)} \tag{5-67}$$

But the value of s (t) is not available befor the calculation, this problem can be solved by assuming:

 $s(t) = y_m (x_{kQ} - x_{oQ})$  (5-68)

where  $y_m$  is the mean value of y in interval  $x \ge (x_0, x_k)$ , which in some cases can be presented as  $\frac{1}{2} (y_{max} + y_{min})$ .

The relative error  $\gamma$  from equations (5-67) and (5-68) can be found as following:

$$\gamma = \frac{\varepsilon_t}{y_m (x_{kQ} - x_{oQ})}$$
(5-69)

By using the total error  $\epsilon_t$  from the preceeding paragraph, we can calculate the relative error  $\gamma$  in each method of integration with unitary and multiple increments.

From equations (5-8), (5-18), (5-28) and (5-69), the relative error  $\gamma$  of rectangular, trapezoidal and three points formula of integration with unitary increments, when the independent variable of integral  $\gamma$  is same as the independent variable t of machine, will be as following:

a - in rectangular method.

$$Y < \frac{1}{y_m} \left| \frac{-1}{2} \frac{(x_k - x_0)}{k} y'(\xi) + \Delta y + \frac{\Delta x + \epsilon_{Tr}}{x_k - x_0} \right|$$
 (5-70)

b - in trapezoidal method.

$$\gamma < \frac{1}{y_{m}} \left| \frac{1}{12} \frac{(x_{Qk} - x_{Q0})^{2}}{k^{2}} y''(\xi) + \Delta y + \frac{\Delta x + \varepsilon_{Tr}}{x_{k} - x_{0}} \right|$$
 (5-71)

c - in three points method.

$$\gamma < \frac{1}{y_{m}} \left| \frac{1}{24} \frac{(x_{Qk} - x_{Qo})^{3}}{k^{3}} y''(\xi) + \Delta y + \frac{\Delta x + \epsilon_{Tr}}{x_{k} - x_{O}} \right| (5-72)$$

The relative error  $\gamma$ , in rectangular, trapezoidal and three points method of integration, with multiple increments, when the independent variable of integral X is same as the independent variable t of machine, from equations (5-13), (5-23), (5-33) and (5-69), will be:

d - in rectangular method.

$$Y < \frac{1}{y_{m}} \left| \sum_{i=1}^{k} \frac{1}{2} \frac{(\delta x)^{2}}{x_{Qk} - x_{Q0}} y'(x) + \Delta y \cdot 2^{h} + \frac{\delta x + \epsilon_{Tr}}{x_{Qk} - x_{Q0}} \right|$$

$$(5-73)$$

e - in trapezoidal method.

$$y < \frac{1}{y_{m}} \left| \sum_{i=1}^{k} \frac{1}{12} \frac{(\delta x)^{3}}{x_{Qh} - x_{Qo}} \right| y''(x) + \Delta y \cdot 2^{h} + \frac{\delta x + \varepsilon_{Tr}}{x_{Qk} - x_{Qo}} \right|$$
(5-74)

f - in three points formula.

$$\gamma < \frac{1}{y_{m}} \left| \sum_{i=1}^{k} \frac{1}{24} \frac{(\delta x)^{4}}{x_{Qk} - x_{Qo}} y^{***}(x) + \Delta y \cdot 2^{h} + \frac{\delta x + \epsilon_{Tr}}{x_{Qk} - x_{Qo}} \right|$$

$$(5-75)$$

The relative error Y, in the rectangular, trapezoidal, and three points formula of integration with unitary increments, when

the independent variable of integral X is a function of the independent variable t of machine, can be found from equations (5-40), (5-50), (5-61) and (5-69) as following:

a - in rectangular method.

$$Y < \frac{1}{y_{m}} \left| \frac{-1}{2} \frac{(t_{k} - t_{0})^{2}}{x_{Qk} - x_{Q0}} Y'(\xi) X'(\xi) + \frac{1}{2} \frac{(t_{k} - t_{0})^{2}}{x_{Qk} - x_{Q0}} Y'(\xi) X'(\xi) + \frac{1}{2} \frac{(t_{k} - t_{0})^{2}}{x_{Qk} - x_{Q0}} \frac{(5-76)}{(5-76)} + \frac{1}{2} \frac{\Delta X + \epsilon_{Tr}}{x_{Qk} - x_{0Q}} \right|$$

b - in trapezoidal method.

$$Y < \frac{1}{y_{m}} \left| \frac{-\frac{(t_{kQ} - t_{oQ})^{3}}{12k^{2}(x_{Qk} - x_{Qo})} \left[ y''(\xi) x'(\xi) - y'(\xi) x''(\xi) \right] + \frac{\Delta x (y_{Qk} - y_{oQ})}{x_{Qk} - x_{Qo}} \right|^{(5-77)} + \left[ \Delta y + \frac{\Delta x (y_{Qk} - y_{oQ})}{x_{Qk} - x_{Qo}} \right] + \left[ \frac{\Delta x + \epsilon_{TT}}{x_{Qk} - x_{Qo}} \right]$$

c - in three points formula.

$$Y < \frac{1}{y_{m}} \left| \frac{(t_{kQ} - t_{oQ})^{4}}{24k^{3}(x_{Qk} - x_{Qo})} \left[ y'''(t) x'(t) - y'(t) x'''(t) \right] + \frac{\Delta x (y_{Qk} - y_{Qo})}{24k^{3}(x_{Qk} - x_{Qo})} + \frac{\Delta x + \varepsilon_{Tr}}{x_{Qk} - x_{Qo}} \right|$$
(5-78)

The relative error  $\gamma$ , in the rectangular, trapezoidal and three points formula, with multiple increments, when the independent variable of integral is a function of the independent variable t of machine, can be found from equations (5-45), (5-55), (5-65) and (5-69) as following:

d - in rectangular method.

$$Y < \frac{1}{y_{m}} \left| \sum_{i=1}^{k} \frac{1}{2} \frac{(\delta t)^{2}}{x_{Qk} - x_{Qo}} \right| Y'(t) x'(t) + (5-79) + (2^{h} \circ \Delta_{Q} y + \frac{\Delta x (y_{Qk} - y_{OQ})}{x_{Qk} - x_{OQ}} \right| + \frac{\delta x + \epsilon_{Tr}}{x_{Qk} - x_{OQ}} - (5-79)$$

e - in trapezoidal method.

$$\left| \frac{1}{y_{m}} \right|_{1}^{\infty} \left| \frac{1}{2} \frac{k}{12(x_{Qk} - x_{oQ})} \left[ y''(t) x'(t) - y'(t) x''(t) \right] + \frac{k}{12(x_{Qk} - x_{oQ})} \left[ \frac{k}{2} \frac{k}{12(x_{Qk} - x_{oQ})} + \frac{k}{2} \frac{k}{2}$$

f - in three points method.

$$\gamma < \frac{1}{y_m} \left| \sum_{i=1}^k \frac{(\delta t)^4}{24(x_{kQ} - x_{oQ})} [y'''(t) x'(t) - y'(t) x'''(t)] + \right|$$

+ 
$$2^{h} \cdot \Delta_{Q}Y + \frac{\Delta x (Y_{Qk} - Y_{Qo})}{x_{Qk} - x_{Qo}} + \frac{\delta x + \epsilon_{Tr}}{x_{Qk} - x_{Qo}}$$

As it is seen, with unitary or multiple increments computation, the relative error is inverse proportional to  $y_m$ . Therefore, in order to reduce the relative error  $\gamma$ , we should increase the value of  $y_m$  by choosing properly the scale factor of y register in the operation of integration.

By increasing the content of y register, we decrease the relative error; and increase the output rates. In general, it can be seen that any effect which increase the output rates will decrease the relative error.

# 5.4. Minimization of the transmission error in unitary or multiple increment computers.

As it was mentioned in chapter 1, by transmitting the output data of some integrators to the inputs dx and dy of integrator k, there is a delay of  $(1+\lambda)T$  between the actual continuous functions X (t), Y (t) and the approximated interpolated, quantized and delayed functions  $f_{iQDx}$  (t),  $f_{iQDy}$  (t), which cause the error of quantization and the error of transmission. The functions X (t), Y (t) are defined as:

$$\begin{cases} x (t) = x [x_0, \delta x_1, \delta x_2, \dots, \delta_{i} x, \dots, (i=1,2,\dots,\infty] \\ \\ y (t) = y [y_0, \delta y_1, \delta y_2, \dots, \delta_{i} y, \dots, (i=1,2,\dots,\infty] \end{cases}$$
(5-82)

and  $f_{iODx}$  (t),  $f_{iODy}$  (t) are expressed as following:

$$\begin{bmatrix} f_{iQDx} (t) = f_{ix} [x_{oQ}, \delta_{1Q}x, \delta_{2Q}x, \dots \delta_{(i-\lambda-1)Q}x, t] \\ f_{iQDy} (t) = f_{iy} [y_{oQ}, \delta_{1Q}y, \delta_{2Q}y, \dots \delta_{(i-\lambda-1)Q}y, t] \end{bmatrix}$$
(5-83)

As it was mentioned in quantization process, the delay =  $\lambda T$ cause the quantization error  $\epsilon_{iQx}$ ,  $\epsilon_{iQy}$  in interval  $t \in (t_i, t_{i+1})$ , which is the difference between the quantized function  $f_{iQx}$ ,  $f_{iQy}$ , and the unquantized function  $f_{ix}$ ,  $f_{iy}$  as following:

$$\begin{bmatrix} \varepsilon_{iQx} = f_{ix} (t) - f_{iQx} (t) \\ \varepsilon_{iQy} = f_{iy} (t) - f_{iQy} (t) \end{bmatrix}$$
(5-84)

where

$$\begin{cases} f_{ix} (t) = f_{ix} [x_0, \delta_1 x, \delta_2 x, \dots \delta_i x, t] \\ f_{iy} (t) = f_{iy} [y_0, \delta_1 y, \delta_2 y, \dots \delta_i y, t] \end{cases}$$
(5-85)

and

$$f_{iQx} (t) = f_{iQx} [x_{oQ}, \delta_{1Q}x, \delta_{2Q}x, \dots \delta_{iQ}x, t]$$
  
$$f_{iQy} (t) = f_{iQy} [y_{oQ}, \delta_{1Q}y, \delta_{2Q}y, \dots \delta_{iQ}y, t]$$
  
(5-86)

The quantization error  $\epsilon_{iQx}$ ,  $\epsilon_{iQy}$ , and the delay of quantization  $\mathcal{T} = \lambda T$  can be minimized by reducting the weight of the quantums  $\Delta x$  and  $\Delta y$ .

The input data which should be available in the input of incremental computer, are the quantized value  $(x_{0Q}, y_{0Q}, \delta_{1Q}x, \delta_{1Q}y, \delta_{2Q}x, \delta_{2Q}y, \ldots, \delta_{1Q}x, \delta_{1Q}y, t)$  But in parallel incremental computer, the data, which are available at the input of machine in the i<sup>th</sup> iteration, are the data of former iteration 1, 2,....i-1 as following:

$$[x_{0Q}, \delta_{1Q}x, \delta_{2Q}x, \dots, \delta_{(i-1)Q}x, t]$$

$$[y_{0Q}, \delta_{10}y, \delta_{20}y, \dots, \delta_{(i-1)Q}y, t]$$
(5-87)

So instead of the approximated interpolated, quantized function  $f_{iQx}$ ,  $f_{iQy}$ , there will be the approximated interpolated, quantized and delayed function  $f_{iQDx}$ ,  $f_{iQDy}$ , whose data have a delay of T (iteration time of integral) with respect to the functions  $f_{iQx}$ ,  $f_{iQy}$ , so the  $f_{iQDx}$ ,  $f_{iQDy}$  are constructed in the input of the incremental computer as following:

$$\begin{bmatrix} f_{iQDx} (t) = f_{iQDx} [x_{oQ}, \delta_{1Q}x, \delta_{2Q}x, \dots \delta_{(i-1)Q}x, t] \\ f_{iQDy} (t) = f_{iQD}y [y_{oQ}, \delta_{1Q}y, \delta_{2Q}y, \dots \delta_{(i-1)Q}y, t] \end{bmatrix}$$
(5-88)

Therefore, in each iteration, there will be a transmission error  $\epsilon_{Tx}$ ,  $\epsilon_{Tv}$  as following:

$$\begin{bmatrix} \epsilon_{Tx} = f_{iQx} [x_{oQ}, \delta_{1Q}x, \delta_{2Q}x, \dots t] - f_{iQDx} [x_{oQ}, \delta_{1Q}x, \delta_{2Q}x, \dots \delta_{2Q}x, \dots \delta_{1Q}x, \delta_{1Q}x, \delta_{2Q}x, \dots \delta_{1Q}x, \delta_{1Q}x, \delta_{2Q}x, \dots \delta_{1Q}x, \delta_{1Q}x, \delta_{1Q}x, \delta_{1Q}x, \dots \delta_{1Q}x, \delta_{1Q}x, \delta_{1Q}x, \dots \delta_{1Q}x, \delta_{1Q}x, \delta_{1Q}x, \dots \delta_{1Q}x, \dots \delta_{1Q}x, \delta_{1Q}x, \dots \delta_{1Q$$

which cause the total transmission error  $\epsilon_{m_{r}}$ .

The block diagram of incremental computer, as it was discussed earlier, is shown in figure (5.3).

In order to suprime the delay T and the transmission error  $\epsilon_{Tr}$ it is sufficient to extrapolate the output increment  $\delta_{i-1}$  of each integrator in parallel incremental computer, in interval  $t \in (t_i, t_{i+1})$ . In this way, the input data of integrator k will be:

 $\begin{bmatrix} x_{0Q}, \delta_{1Q}x, \delta_{2Q}x, \dots, \delta_{1Q}x, t \\ Y_{0Q}, \delta_{1Q}y, \delta_{2Q}y, \dots, \delta_{1Q}y, t \end{bmatrix}$  (5-90)

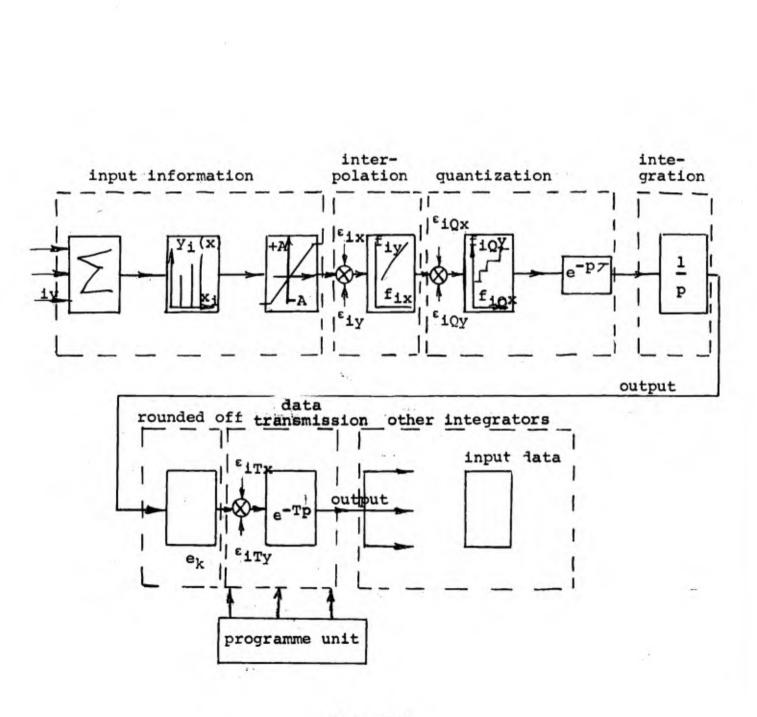


fig. 5.3.

Block diagram of I.C.

249

instead of the data:

$$\begin{bmatrix} x_{0Q}, \delta_{1Q}x, \delta_{2Q}x, \dots, \delta_{(i-1)Q}x, t \\ y_{0Q}, \delta_{1Q}y, \delta_{2Q}y, \dots, \delta_{(i-1)Q}y, t \end{bmatrix}$$
(5-91)

The transfer function of extrapolators  $F(p) = e^{pT}$ , which use the output data of integrators  $\delta_{1Q}s$ ,  $\delta_{2Q}s$ ,...,  $\delta_{(i-1)Q}s$  is defined as:

$$F(p) = F(\delta_{10}s, \delta_{20}s, \delta_{30}s, \dots, \delta_{(i-1)0}s) \quad (5-92)$$

The new block diagram of incremental machine is drawn in figure (5.4).

In serial incremental computer, in proper way of programming, it is possible to suppress the delay T and transmission error  $\epsilon_{\rm Tr}$ between integrators. If n integrators are connected in cascade (fig 5.5) and are proceeded in the same direction, then the input data of each integrator in i iteration (except the first one) will be as following:

$$\begin{bmatrix} \mathbf{x}_{0Q}, \ \delta_{1Q}\mathbf{x}, \ \delta_{2Q}\mathbf{x}, \dots, & \delta_{1Q}\mathbf{x} \\ \mathbf{y}_{0Q}, \ \delta_{1Q}\mathbf{y}, \ \delta_{2Q}\mathbf{y}, \dots, & \delta_{1Q}\mathbf{y} \end{bmatrix}$$
(5-93)

For instant, the input by of integrator 2 has the information of ith iteration of integrator 1 as following:

$$(\delta_1 s)_i = (\delta_2 y)_i$$
 (5-94)

$$\begin{cases} f_{iQy} = T \\ f_{iQy} = f_{iQy} (Y_0, \delta_{1Q}Y, \delta_{2Q}Y, \dots, \delta_{iQ}Y) \end{cases}$$

In this case, because the data of i<sup>th</sup> iteration exist in the input of all integrators (except the first one), there is no transmission error and there is no need of extrapolation block.

Example: In order to generate  $Y = \cos \omega t$  the interconnection diagram is as figure (5.6).

In the figure (5.6), the integrators are connected in cascade and proceeded in the same direction. Therefore, the following difference equation may be written for each integrator.

integrator 1	$\left[ \left( \nabla \mathbf{I}_{1} \right)_{\mathbf{i}} = \left( \mathbf{Y} \right)_{\mathbf{i}} \cdot dt$		
	$Y_i = Y_{i-1} + (\nabla Y)_i$		(5-95)
÷	$\begin{bmatrix} (\nabla \mathbf{I}_{1})_{\mathbf{i}} = (\mathbf{Y})_{\mathbf{i}} \cdot dt \\ \mathbf{Y}_{\mathbf{i}} = \mathbf{Y}_{\mathbf{i}-1} + (\nabla \mathbf{Y})_{\mathbf{i}} \\ (\nabla \mathbf{Y})_{\mathbf{i}} = - (\nabla \mathbf{I}_{3})_{\mathbf{i}-1}. \end{bmatrix}$		
integrator 2	$\left[ \left( \nabla \mathbf{I}_{2} \right)_{\mathbf{i}} = \omega^{2} \cdot \left( \mathrm{dx} \right)_{\mathbf{i}} \right]$		
	$ \begin{bmatrix} (\nabla \mathbf{I}_2)_{\mathbf{i}} = \omega^2 \cdot (\mathbf{d}\mathbf{x})_{\mathbf{i}} \\ (\mathbf{d}\mathbf{x})_{\mathbf{i}} = (\nabla \mathbf{I}_1)_{\mathbf{i}} \end{bmatrix} $		(5-96)

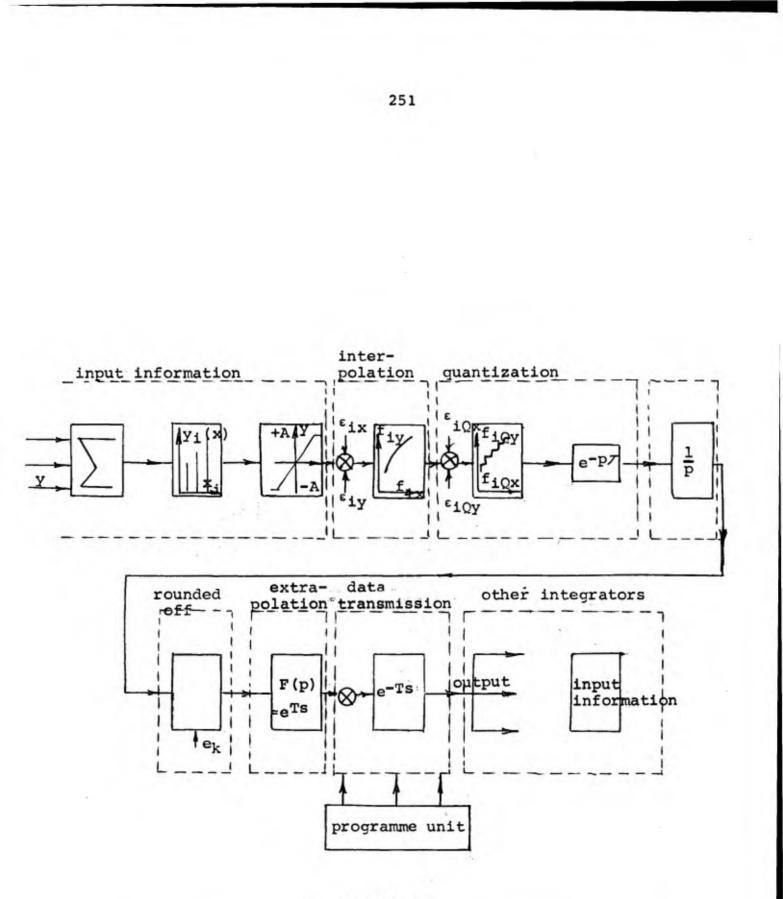
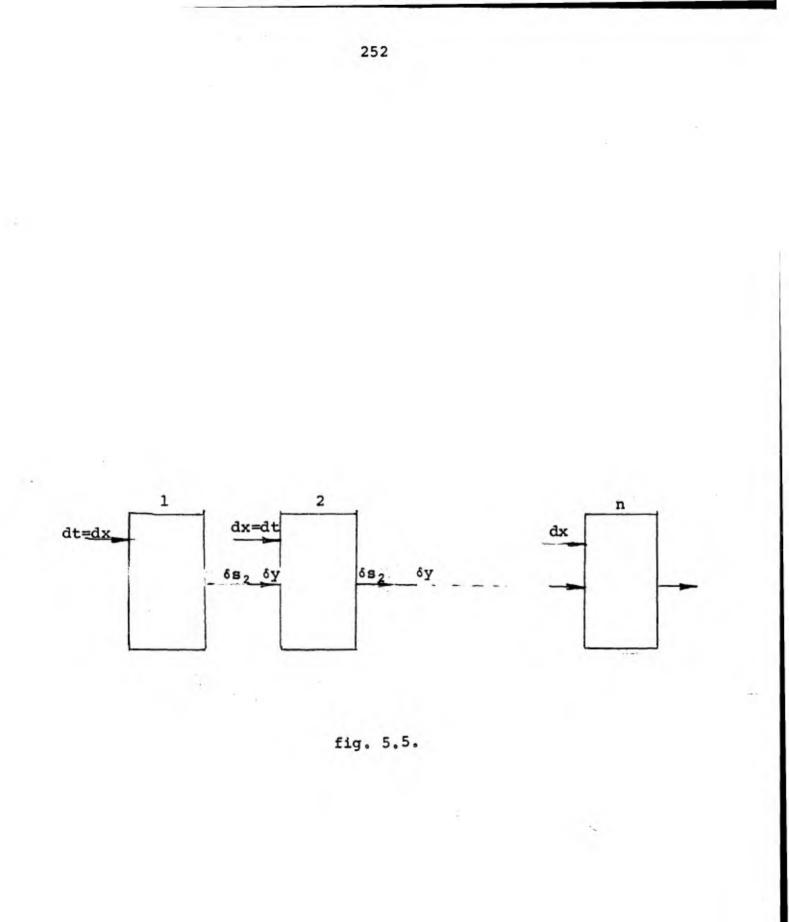


fig. 5.4.

Block diagram of I.C. by adding the extrapolation block X.



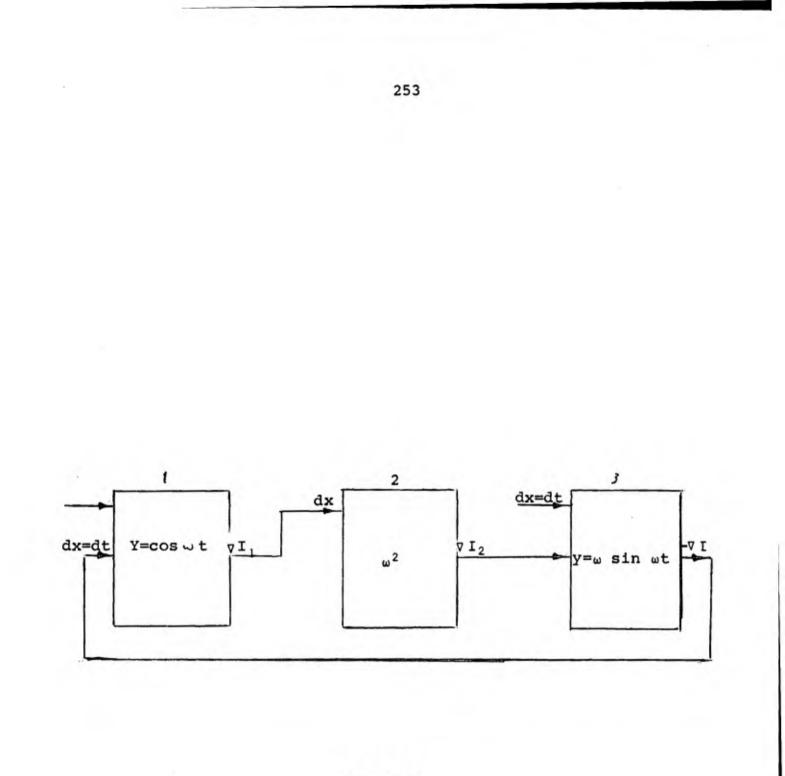


fig. 5.6.

and for  
integrator 3
$$\begin{cases}
\left(\nabla I_{3}\right)_{i} = Y_{i} \quad (dt) \\
Y_{i} = Y_{i-1} + \left(\nabla Y\right)_{i} \quad (5-97) \\
\left(\nabla Y\right)_{i} = \left(\nabla I_{2}\right)_{i}
\end{cases}$$

The equations (5-95), (5-96) and (5-97) can be written as:

$$\begin{cases} (\nabla I_{1})_{i} = Y_{i} \cdot dt \\ Y_{i} = Y_{i-1} - (\nabla I_{3})_{i-1} \end{cases}$$
(5-98)

$$(\nabla I_2)_1 = \omega^2 \cdot (\nabla I_1)_1$$
 (5-99)

$$\begin{pmatrix} (\nabla I_3)_i = Y_i & dt \\ \\ Y_i = Y_{i-1} + (\nabla I_2)_i \end{pmatrix}$$
 (5-100)

by finding the  $Y_1$  and taking its Z transform, from equations (5-98), (5-99) and (5-100), the Y (z) will be:

$$Y(z) = \frac{Y(0)(1-z^{-1})}{1-2(1-\omega^{2T^{2}})z^{-1}+z^{-2}}$$
(5-101)

The inverse Z transform of equation (5-101) will be:

Y (iT) = 
$$\frac{Y(0)}{(1 - \omega^2 \frac{T^2}{4})^{1/2}} \cos(i\Omega T + \lambda)$$

$$\Omega = \frac{1}{T} \arctan tg \frac{\omega T (1 - \omega^2 \frac{T^2}{4})^{\frac{1}{2}}}{1 - \omega^2 \frac{T^2}{2}}$$
(5-102)
$$\lambda = \arctan tg \frac{2 \omega T}{4 - \omega^2 T^2}$$

As it can be seen from equation (5-102), if T becomes very small  $T \rightarrow 0$ . Then, amplitude, frequency and phase of the approximated function Y (iT) will be very near to the original function Y = cos  $\omega T$  as following:

$$\begin{aligned} \mathbf{T} &= \mathbf{O} \\ \mathbf{\Omega} &= \left| \frac{1}{T} \arctan tg \frac{\omega T \left(1 - \omega^2 \frac{T^2}{4}\right)^{\frac{1}{2}}}{1 - \omega^2 \frac{T^2}{2}} \right|_{\mathbf{T} \to \mathbf{O}} \end{aligned}$$
(5-104)  
$$= \left| \frac{1}{T} \arctan tg \frac{\omega T \left(1 - \omega^2 \frac{T^2}{4}\right)^{\frac{1}{2}}}{1 - \omega^2 \frac{T^2}{2}} \right|_{\mathbf{T} \to \mathbf{O}} \end{aligned}$$
(5-105)  
$$\left| \frac{\mathbf{Y} \left(\mathbf{O}\right)}{\left(1 - \omega^2 \frac{T^2}{4}\right)^{\frac{1}{2}}} \right|_{\mathbf{T} \to \mathbf{O}} \end{aligned}$$
(5-105)

$$\lambda = \left[ \arctan tg \frac{2 \omega T}{4 - \omega^2 T^2} \right]_{T=0} \simeq 0 \quad (5-106)$$

so the equation (5-102) can be written as:

Y (iT) 
$$\simeq \cos (\omega iT)$$
 (5-107)

Of course, the small difference which exist between the original function  $Y = \cos \omega t$  and the equation (5-102) is, because of the errors of method, the quantization, round off, of the integrators and also the transmission error in the first integrator.

In the same problem, if the integrators are series coscaded in reverse direction, the interconnection diagram will be as figure (5.7).

The increments  $(\nabla I_1)_{i-1}$ ,  $(\nabla I_2)_{i-1}$  and  $(\nabla I_3)_{i-1}$  are in the input of integral, in the i<sup>th</sup> iteration. So it is like the parallel incremental computer which use the data with the time delay of T (machine cycle). Therefore there will be the error of transmission in each integral as following:

in integrator l	$\begin{bmatrix} \varepsilon_{Tx} = 0 & (f_{iQx} = t) \end{bmatrix}$	(5-108)
	$\begin{cases} \varepsilon_{Tx} = 0 & (f_{iQx} = t) \\ \varepsilon_{Ty} = (\nabla I_2)_{i-1} - (\nabla I_2)_i \end{cases}$	
in integrator 2	$\begin{cases} \varepsilon_{Tx} = (\nabla I_1)_{i-1} - (\nabla I_1)_{i} \\ \varepsilon_{Ty} = 0 \end{cases}$	(5-109)
	$\epsilon_{TY} = 0$	
in integrator 3	$\begin{cases} \varepsilon_{Tx} = 0 \\ \varepsilon_{Ty} = (\nabla I_3)_i - (\nabla I_3)_{i-1} \end{cases}$	(5-110)
	$ \left[ \varepsilon_{\mathrm{TY}} = (\nabla \mathbf{I}_{3})_{\mathbf{i}} - (\nabla \mathbf{I}_{3})_{\mathbf{i}-\mathbf{l}} \right] $	

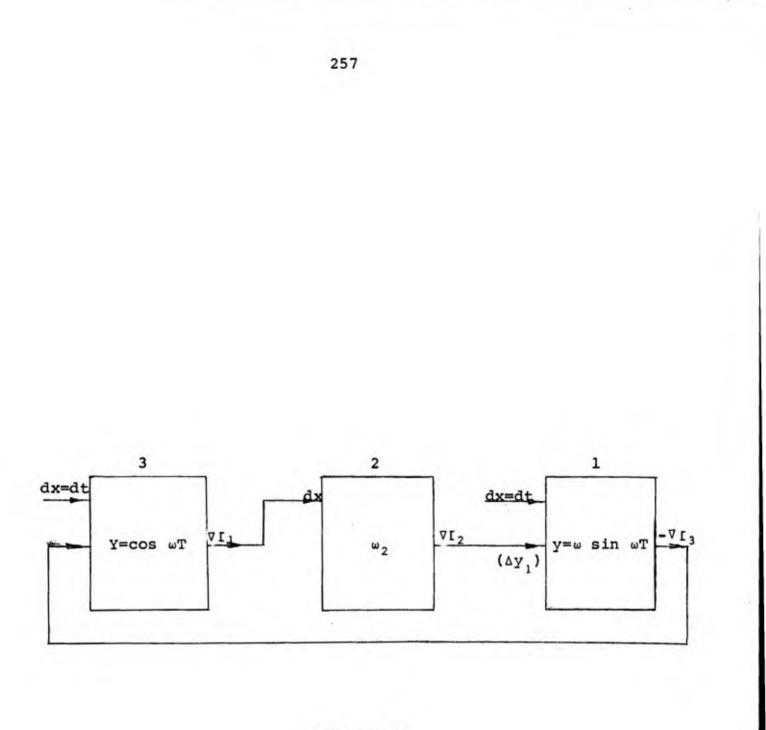


fig. 5.7.

The difference equation of the integrators will be:

integrator 1  

$$\begin{pmatrix} (\nabla I_{3})_{1} = Y_{1} \cdot (dt)_{1} \\ Y_{1} = Y_{1-1} + (\nabla Y)_{1-1} & (5-111) \\ (\nabla Y_{1})_{1} = (\nabla I_{2})_{1-1} & (5-111) \\ (\nabla Y_{1})_{1} = (\nabla I_{2})_{1-1} & (5-112) \\ (dx_{2})_{1} = (\nabla I_{1})_{1-1} & (5-112) \\ (dx_{2})_{1} = (Y_{1})_{1-1} & (5-112) \\ (\nabla Y_{1})_{1} = Y_{1} \cdot (dt)_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla I_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla I_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla I_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (5-113) \\ (\nabla Y_{3})_{1} & (0, 1) \\ (\nabla Y_{3})_{1} = - (\nabla Y_{3})_{1} & (0, 1) \\ (\nabla Y_{3})_{1} & ($$

by solving the equations (5-111), (5-112) and (5-113), and taking the Z transform of them, the result will be:

Y (z) = 
$$\frac{Y(0)(1-z^{-1})}{z^{-2}(1+\omega^2T^2)-2z^{-1}+1}$$
 (5-114)

(5 - 115)

and its inverse Z transform is expressed as:

Y (iT) = Y (0) 
$$e^{i \log (1+\omega^2 T^2)} \cos (i \arctan tg \omega T)$$

As it is seen from equation (5-115), the generation of function Y (iT) = Y (0) cos  $\omega T$ , is followed by the generation of an exponential function  $e^{i \log 1 + \omega^2 T^2}$ , which is the result of accumulation of the transmission error  $\epsilon_{Tx}$  and  $\epsilon_{Ty}$  in each iteration.

It is seen, when the integrators are series coscades in the same direction which they are process, the data in the input of each integrator are:

and

δ<sub>10</sub>x, δ<sub>20</sub>x,.... δ<sub>10</sub>x

so there is no delay T of transmission data which has existed in the parallel incremental computer, therefore, there is no transmission error  $\epsilon_{TT}$ .

In the second case, where the integrators are series coscaded in reverse direction, because in the input of each integrator in i<sup>th</sup> iteration; there is the information of former iteration:  $\delta_{1Q}x$ ,  $\delta_{2Q}x$ ,...  $\delta_{(i-1)Q}x$  and  $\delta_{1Q}y$ ,  $\delta_{2Q}y$ ,...  $\delta_{(i-1)Q}y$ , there will be a delay of T in the transmission of data, which cause the transmission error  $\epsilon_{Tx}$  and  $\epsilon_{Ty}$  in each iteration as it was the case in the parallel incremental computer. The conclusion is that, in order to minimize the transmission error in serial machine, the programming should be in such a way that in the i<sup>th</sup> iteration, the input of most integrators use the data of the i<sup>th</sup> iteration. Those integrators which cannot receive the data of the i<sup>th</sup> iteration, should use an extrapolator unit as it was the case in the parallel incremental computer.

If the output extrapolation value of each integrator is called  $\delta_{QE}^{*}$  s, then the above discussion can be formulated in the following form:

$$\delta_{QE}^{*} s_{ji} = A_{jK} \delta_{i} s_{Q}^{*} + B_{(j-K)} \delta_{QE} s_{i}^{*}$$
(5-116)

where

$$\begin{cases} A_{j-K} = 1 & \text{if } j-K > 0 \\ A_{j-K} = 0 & \text{if } j-K \le 0 \end{cases}$$
(5-117)  
$$\begin{cases} B_{j-K} = 1 & \text{if } j-K \ge 0 \\ B_{j-K} = 0 & \text{if } j-K < 0 \end{cases}$$
(5-118)

and  $\delta_1 \leq S_Q^{\times}$  is the approximated interpolated and quantized value of integral function. By taking into account the above discussion and the equations (5-116), (5-117) and (5-118), the block diagram of serial incremental computer can be represented as in the figure (5.8).

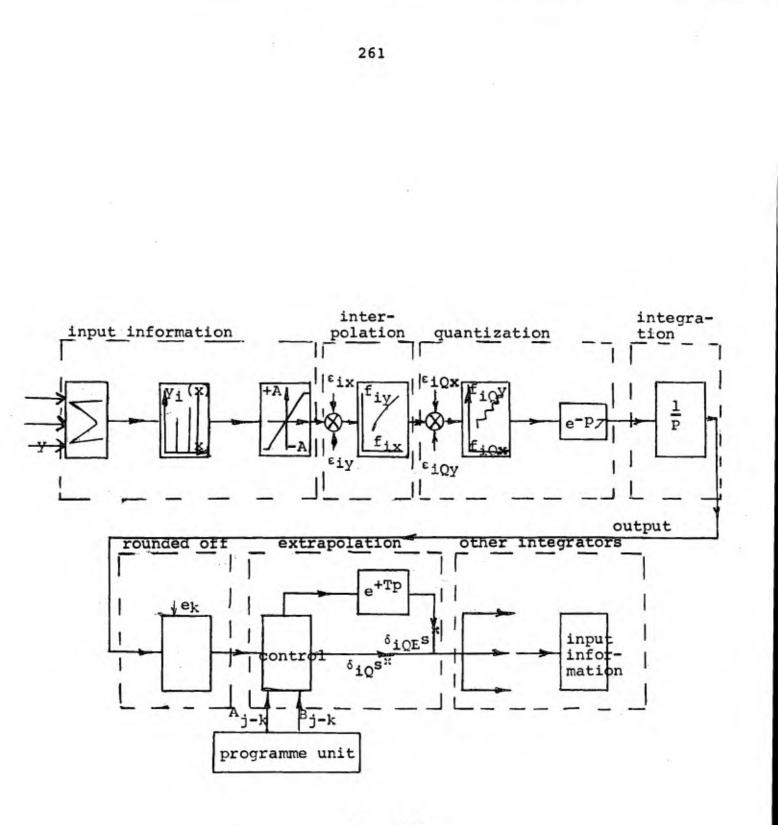


fig. 5.8.

The block diagram of serial I.C. with the extrapolation

### 5.5 Choice of number of bits in multiple increment computer.

As it was seen, the value of integral function S (t) is replaced by the approximated interpolated quantized and rounded off value of integral S  $_{Om}$  (t) with the error  $\epsilon_{t}$  as :

$$\epsilon_t = S (t) - S_{\zeta m}^{\nu} (t)$$
 (5-119)

and the relative error is defined us :

 $Y = \frac{\xi_t}{S(t)}$ (5-120) If G(p) and  $\tilde{G}$  (p) are transfer function of desired value of integration and approximated one, then the relative error Y (p) will be  $Y(p) = \frac{G(p) - G(p)}{G(p)} = 1 - \frac{\tilde{G}(p)}{G(p)}$ (5-121)

for  $p = J\omega$ , the value of  $G(\omega) = \frac{1}{\omega}$  so absolute value of relative error  $Y(\omega)$  will be

 $\bar{\mathbf{y}}(\omega) = \mathbf{1} - \omega \mathbf{G}(\omega) \tag{5-122}$ 

In order to find the relative error  $\Upsilon(\omega)$ , we should find the transfer function of approximated value of integration  $G^{"}(\omega)$ , for each method of integration.

As for multiple incremental computation, the degree of interpolation function is more than one, therefore we calculate the transfer function of first degree (trapezoidal) and higher degree interpolation formula for approximate integration. The numerical realization of integration by the trapezoidal method, is:

$$\delta_{i} S_{Q}^{H}(t) = \begin{cases} t & (5-123) \\ i(t) \circ dt \\ t - T & \\ \frac{T}{2} [i^{H}(t) - i^{H}(t - T)] \end{cases}$$

if the integral is realized by this numerical method, the calculations are performed by the following recurrence relations:

$$s_Q^{\chi}(t) = s_Q^{\chi}(t - T) + \frac{T}{2} [i^{\chi}(t) + i^{\chi}(t - T)]$$
 (5-124)

its transfer function will be:

$$G^{*}(p) = \frac{T}{2} \circ \frac{1 + e^{-pT}}{1 - e^{-pT}}$$
 (5-125)

substituting  $j \omega$  for p and  $e^{\pm j\omega t} = \cos \omega t + j \sin \omega t$  in equation (5-125), we get:

$$G^{H}(j\omega) = -j \frac{T}{2} \cot \frac{\omega T}{2}$$
(5-126)  

$$A^{H}(\omega) = |G^{H}(\omega)|$$
(5-127)  

$$= \left| \frac{T}{2} \cot \frac{\omega T}{2} \right|$$
(5-127)

$$\theta^{\varkappa}(\omega) = -\frac{\pi}{2}$$

The ratio of transfer function of the approximate formula  $A^{H}(\omega)$  (eq. 5-127) to the transfer function of the desired one

$$\frac{A^{\#}(j\omega)}{G(j\omega)} = \frac{\frac{T}{2} \cot \frac{\omega T}{2}}{\frac{1}{\omega}}$$

$$= \frac{\omega T}{2} \circ \cot \frac{\omega T}{2}$$
(5-128)

for three points formula of integration:

(5-129)

$$\delta_{1}S_{0}^{*} = \int_{t-T}^{t} (t) \cdot dt$$

$$= T [i^{*}(t) + \frac{1}{2}\Delta i(t) + \frac{1}{12} [\Delta i(t) - \Delta i(t + 1)]$$
(5-130)

by putting:

$$\Delta i(t) = i(t - T) - i(t)$$
(5-131)

$$\Delta i(t + T) = i(t) - i(t + T)$$

in equation  $(5-13\rho)$  we get:

$$\delta_{i} S_{Q}^{x}(t) = T \left[\frac{1}{3}i(t) + \frac{7}{12}i(t-T) + \frac{1}{12}i(t+T)\right] (5-132)$$

Writting the recurance equation we will have:

$$s_Q^{\times}(t) = s_Q^{\times}(t - T) + T[\frac{1}{3}i(t) + \frac{7}{12}i(t - T) + \frac{1}{12}i(t - T) + \frac{1}{12}i(t + T)]$$
  
(5-133)

12

Taking the transfer function of equation (5-133) we get:

$$G^{*}(p) = \frac{s^{*}(p)}{I(p)}$$

$$= T \frac{\frac{1}{3} + \frac{1}{12} e^{pT} + \frac{7}{12} e^{-pT}}{1 - e^{-pT}}$$
(5-134)

by puting :

$$p = j\omega$$

and

$$e^{ij\omega t} = \cos \omega t \pm j \sin \omega t$$

the equation (5-134) can be written as:

$$\mathbf{A}^{\mathsf{H}}(\mathbf{j}\omega) = |\mathbf{G}^{\mathsf{H}}(\mathbf{j}\omega)| \qquad (5-135)_{\mathrm{eq}}$$

$$= \frac{T}{24} \frac{\sqrt{28}\cos^2 \omega T + 16}{\sin \frac{\omega T}{2}}$$

The ratio of the approximate formula  $A^{X}(j\omega)$ , (eq.5-135 to the transfer function of the derived one G (jw), is as following

$$\frac{A^{*}(j\omega)}{G(j\omega)} = \frac{\frac{T}{24} \frac{\sqrt{28 \cos^{2} \omega T + 16 \cos \omega T + 52}}{\frac{\sin \omega T/2}{\omega}} (5-136)$$

$$= \frac{\omega T}{12} \frac{\sqrt{7 \cos^{2} \omega T + 4 \cos \omega T + 13}}{\frac{\sin \frac{\omega T}{2}}{\omega}}$$

We will now consider Simpson's method of integration. This formula is obtained by integrating the second order interpolation polynomial which coincides with the abscissae at points t, t-T, t-2T and with discrete values of the input signal at these instants.

The difinite integral is calculated by the equation:

$$\delta_{i} S_{Q}^{H}(t) = \begin{cases} t & (t) \cdot dt \\ t-2T & (5-137) \end{cases}$$

 $= \frac{T}{3} [i^{*}(t) + 4i^{*}(t - T) + i^{*}(t - 2T)]$ 

If this method is employed, the following recurrence relation is used:

 $I^{X}(t) = I(t - 2T) + \frac{T}{3}[i(t) + 4i^{X}(t - T) + i^{X}(t - 2T)]$ 

(5 - 138)

The transfer function is:

$$G^{*}(p) = \frac{T}{3} \frac{1+4 e^{-pT} + e^{-2pT}}{1-e^{2pT}}$$
 (5 - 39)

substituting jw for p and e  $jwt = \cos wt + j \sin wt in formula$ (5 - 139), we get

 $G^{X}(j\omega) = -j \frac{T}{3} \cdot \frac{2 + \cos \omega T}{\sin \omega T} \qquad (5 - 140)$   $A^{X}(\omega) = G^{X}(j\omega)$   $= \left| \frac{T}{3} \cdot \frac{2 + \cos \omega T}{\sin \omega T} \right| \qquad (5 - 141)$   $\Theta^{X}(\omega) = -\frac{\pi}{2} \qquad (5 - 142)$ 

The ratio of transfer function  $A^{2}(\omega)$  of the approximate formula (eq.5-141 to the transfer function of the desired one is :

 $\frac{A^{X}(\omega)}{G(\omega)} = \frac{T\omega}{3} \cdot \frac{2 + \cos \omega T}{\sin \omega T}$  (5 - 143)

Then the relative error for each method of integration is:

1 - in trapezoidal method:

$$\gamma(\omega) = 1 - \frac{\omega t}{2} \cot \frac{\omega t}{2}$$
 (5-144)

2 - in three points method:

$$\gamma(\omega) = 1 - \frac{\omega t}{12} \frac{\sqrt{7\cos^2 \omega t + 4\cos \omega t + 13}}{\sin \frac{\omega t}{2}} (1-145)$$

3 - in Simpson's method:

$$\gamma(\omega) = 1 - \frac{\omega t}{3} \frac{2 + \cos \omega t}{\sin \omega t}$$
(5-146)

If the  $w_c$  is the maximum frequency that can pass in the linear part of characteristic frequency of incremental computer, without producing the distortion and phase shift at the output, then we will have:

$$y = A^{\circ} \sin \omega_{c} t$$
 (5-147)

so the increment &y, which is applied to the input of increment computer is:

$$\frac{\delta y}{\delta t} = \frac{dy}{dt}$$
(5-148)

= 
$$A^{\circ} \omega^{\circ} \cos \omega_{c} t$$
 (5-149)

or

$$\delta y = A \circ \omega \circ \delta t \cos \omega_c t \qquad (5-150)$$

where  $\delta t$  is the machine cycle T, from equation (5-150), the y is maximum when  $\cos \omega_{_{\rm C}} t$  = 1

(A) max is the maximum capacity of Y register of the incremental computer, which is equal to:

$$(A)_{max} = 2^{n} - 1 \tag{5-152}$$

where n is the number of bits of Y register. The maximum value of increments  $(\delta y)_{max}$  is equal to:

$$(\delta y)_{max} = 2^n - 1$$
 (5-153)

where h is the number of bits in incremental register  $\delta y_{\circ}$  By putting the value of (A)<sub>max</sub> and  $(\delta y)_{max}$  from equations (5-152), (5-153), in equation (5-151), we will have:

$$2^{11} - 1 = T^{\circ} \omega_{c} (2 - 1)$$
 (5-154)

The maximum frequency  $\omega_{c}$  can be found from equation (5-154) as

following:

for

for

$$\omega_{c} = \frac{2^{n} - 1}{(2^{n} - 1)T}$$
(5-155)

The choice of the number of bits in incremental register h, can be made as high as it is desired in such a way that the error  $\gamma(\omega)$  does not become greater than the quantum  $\Delta y = 2^{-n}$ , so:

$$\gamma(\omega) < 2^{-n}$$
 (5-156)

By using the equation (5-156) for the error of different method of integration, the relation between h and n can be found by expanding the equation (5-144), (5-145) and (5-146) in  $\omega$  and taking the first term of expansion as the approximation. By putting its maximum value  $\omega_{\rm C}$  from equation (5-155) in them, then for trapezoidal method of integration we will have:

$$2^{h} < \sqrt{3} 2 \frac{n+2}{2}$$
 (5-157)  
 $n = 10$   
 $h < 6$ 

For three points and Simpson's method, we will have:

$$2^{h} < 1 \circ 15 \times 2^{\frac{3n+3}{4}}$$
  
 $n = 10$   
 $h < 8$   
(5-158)

From the equations (5-157) and (5-158), it is seen that it is possible to increase the speed of integration by increasing the number of increment bits h from 1 to 8 bits for n = 10, in such a way that the error does not exceed the quantum  $\Delta y_{\circ}$ . But it is seen that the maximum value of h does not change too much from the first degree interpolation (trapezoidal method) to the second degree interpolation. The reason is that, when we increase the step of integration and the degree of interpolation, although we reduce the error of method, we do not reduce the quantization error  $\varepsilon_{\pm 0}$ .

It was discussed in chapter (3), the quantization error is practically same in all methods of integration. That is why, by using more accurate formula of integration, although the error of method is reduced, the total error  $\varepsilon_t$  will reduce slightly. So the remide of reducing the total error is reducing the quantization error as well. The best choice between the method of integration and the number of increment bits h, is to choose the error of method  $\Gamma(t)$ approximately equal to the quantization error  $\varepsilon_{tO}$  (t).

# 5.6. Choice of algorithms for unitary and multiple increment computers.

In order to choose the algorithms of unitary and multiple increments computers, with a desired accury, we should know the errors of the algorithms.

The error which exist in incremental computation is the sum of the error of method  $\Gamma(t)$ , the quantization error  $\varepsilon_{tQ}$ , the round off error  $e_k$ , and the transmission error  $\varepsilon_{Tr}$ . In the preceeding chapters, we have also seen how to reduce all these partial errors.

It is possible to reduce the error of method  $\Gamma(t)$  as low as possible, by choosing the high degree interpolation formula. It was also seen that, it is not worthwhile to use higher than first degree interpolation formula for unitary incremental computation (ref. chapter 2).

The quantization error  $\epsilon_{tQ}$  does not depend on the method of integration. It only depends on the quantums  $\Delta x$ ,  $\Delta y$  and on the number of bits h in  $\delta X$  register.

The round off error e of increment computers can be minimized by the appropriate initial condition.

The transmission error can be minimized by adding the extrapolator blocks in parallel increment computers. In serial increment computers, the transmission error can be minimized by the proposed method of programming in the chapter 4. The total error  $\varepsilon_t$  is the sum of error of method  $\Gamma(t)$ , the quantization error  $\varepsilon_{tQ}$ , the round off error  $\varepsilon_k$ , and the transmission error  $\varepsilon_{mr^2}$ 

The choice of algorithms in incremental computation must be made in such a way that, with minimal equipment and calculating time, the total error  $\varepsilon_+$  does not exceed a certain limit.

We resume here the rectangular, trapezoidal methods, and their partial resulting errors, as well as the total errors and the algorithms.

In rectangular method of integration in unitary increment computers, the error of method  $\Gamma(t)$  is:

$$r(t) < \frac{7}{2}(x_{k} - x_{0}) \Delta y$$

The quantization error  $\varepsilon_{tO}$  is:

$$\varepsilon_{\pm 0} < \Delta y (x_k - x_0)$$

Then the total error  $\varepsilon_+$  is:

$$\varepsilon_t < \frac{9}{2} \Delta y (x_k - x_o) + e_k + \varepsilon_{Tr}$$

and the algorithms of integration are:

$$s_Q^{\prime\prime}$$
 (t) =  $\sum_{i=1}^{k} Y_{iQ} \circ \Delta_{iQ} x$ 

$$y_{iQ} = y_{(i-1)Q} + \frac{b}{\sum_{j=1}^{b} (\Delta_{iQ}y)_{j}}$$

where b is the number of Ay input of incremental computer.

In trapezoidal method of integration in unitary increment computers, the error of method  $\Gamma(t)$  is:

$$r(t) < \frac{7}{12} (x_k - x_0) \Delta y$$

The quantization error  $\varepsilon_{\pm 0}$  is:

$$\varepsilon_{to} < \Delta y (x_k - x_o)$$

Then the total error  $\varepsilon_+$  is:

$$\varepsilon_t < \frac{19}{12} \Delta y (x_k - x_o) + e_k + \varepsilon_{Tr}$$

and the algorithms of integration are:

$$\begin{bmatrix} s_{Q}^{\times} (t) = \sum_{i=1}^{k} (y_{iQ} \circ \Delta_{iQ}x + \frac{1}{2} \Delta_{iQ}y \circ \Delta_{iQ}x) \\ y_{iQ} = y_{(i-1)Q} + \sum_{i=1}^{k} (\Delta_{iQ}y) \end{bmatrix}$$

In unitary increment computers, by comparing the total error  $\varepsilon_t$  in rectangular and trapezoidal method of integration, it is seen

that, the total error  $\varepsilon_t$  in trapezoidal method is 2.5 time smaller than the rectangular method. It is also seen that, it is not worthwhile to use an higher degree interpolation formula than the first degree interpolation.

For instance by choosing the three points method of integration, we have:

$$s^{x} (t) = \sum_{i=1}^{k} \left| \begin{bmatrix} y_{iQ} & a_{iQ}x + \frac{1}{2} a_{iQ}x & a_{iQ}y + \frac{1}{2} a_{iQ}x & a_{iQ}y + \frac{1}{2} a_{iQ}y & a_{iQ}y \end{bmatrix} + \left( \frac{1}{12} a_{iQ}y & a_{iQ}y + \frac{1}{2} a_{iQ}y & a_{iQ}y + \frac{1}{2} a_{iQ}$$

In this equation, the quantities in paranthesis are smaller than the quantum  $\Delta y$  of the function y(x). Therefore, the computer cannot detect and operate on it. So, when accurate algorithms are needed for unitary incremental computation, the algorithms of the trapezoidal method of integration are more convenient.

In multiple incremental computation, the step of integration  $\delta x$ is  $2^r$  time larger than ( $\delta x = 2^r \cdot \Delta x$ ) unitary increment computation. Therefore, the algorithms of this computation should be chosen with higher precision.

The three points integration formula gives a good approximation for multiple increment computation. The algorithm of this method is:

$$\begin{cases} s_{\Omega}^{\pi}(t) = \sum_{i=1}^{k} [y_{iQ} \circ \delta_{iQ}x + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}x + \frac{1}{2}$$

We have seen in (5 - 5) that it is possible to increase the step of integration  $\delta x$  to six bits, for ten bits Y register, in such a way that, the total error  $\varepsilon_{\pm}$  does not exceed the quantum  $\Delta y$ .

For larger integration step  $\delta x$ , we can choose the third, fourth, and even higher degree interpolation formula. But, by increasing the degree of the interpolation formula, the algorithm becomes more complex, so it needs too much equipment and calculating time. On the other hand, by increasing the step of integration, the quantization error  $\varepsilon_{tQ}$  is increased as well, and in some cases, the quantization error  $\varepsilon_{tQ}$  may dominate the total error  $\varepsilon_{+}$ .

Therefore, by choosing the step of integration  $\delta x$ , the most economical and convenient way of choosing the algorithm of machine is, to select a degree of interpolation formula, and the quantum  $\Delta y$ , in such a way that, the error of method  $\Gamma(t)$ , becomes equal to the quantization error  $\varepsilon_{tO}$ .

## CHAPTER VI

## A NEW TYPE OF UNIVERSAL INCREMENTAL COMPUTER.

## DESIGN - DEVELOPMENT - CONSTRUCTION.

#### Introduction.

The principal concept of the design philosophy of a new type of incremental computer is the realization of a high speed digital machine which is operationally analogous to a analogue computer with the accuracy and reliability of a digital machine. This computer performs the mathematical and logical operation of digital computer as well. Therefore this machine, successfuly combines the principal operation and advantages of digital and analogue computer.

The basic operation of this machine is the integration, summation, and multiplication of functions.

It also operates various mathematical operations as addition,

substraction, multiplication, division as well as logical decision making.

The incremental computers can effectively be used for solving a system of linear or nonlinear differential equations, as well as algebraic, integral, trigonometric equations or other problems, associated with calculation of continuous functions.

The incremental computers may be devided into two types, serial and parallel:

In serial machine, the integration and others operations are sequentially performed one after another in the serial form. In this computer, the common units are: the memory unit, arithmetic unit, control unit and input output units.

The common memory permits the continuous solution of a mathematical problem to be achived serially, that is, one operation after another by means of only one arithmetic unit. Such a serial method of solving a problem by means of a single integrator in an analogue computer is in principle possible, if a suitable analogue memory is available.

In serial incremental computer, the interconnection of integrators into a sequence required for a problem solution, is achieved by means of a programme which may be fixed or altered, according to the result obtained during the problem-solving process.

In other words, this machine may achieve conditional or unconditional transfer from one programme to another in a manner similar to that achieved in a conventional digital computer. From the standpoint of economy, serial machines are conveniently used for solving complex nonlinear problems, requiring a relatively large number of integrators. It possess wide possibility for execution of various logic operations. The speed of problem solution in a serial machine depends on the complexity of the problem.

A parallel incremental computer consists of a number of individual digital integrators, each integrator being a self-contained unit operating independently of the other integrators. In a parallel machine, it is not necessary to have a common memory unit and an arithmetic unit. Interconnection of individual integrators are performed by a plugboard. The advantage of the parallel incremental computer is high computing speed, which does not depend on the complexity of the problem. This speed advantage is achieved at the expense of more integrators and, consequently, leads to a considerable increase in the electronic equipment. Thus, the use of parallel incremental computer is advisable only in those cases where an extremely high computation rate is necessary.

Combination of the problem-solving principal used in analogue computers and the digital technique permits to achieving in both types of incremental computers, an essentially new type of digital computer, possessing advantages of both analogue and digital machines.

On the other hand, the use of incremental computers permits a substantial increase in accuracy of computation, in flexibility and general applicability of the computer, as well as in simplicity of design and economy.

The incremental computer which is devised by the author in the industrial electronic laboratory of Brussel University is a new type of serial incremental computers.

# 6-1. Organization of computer.

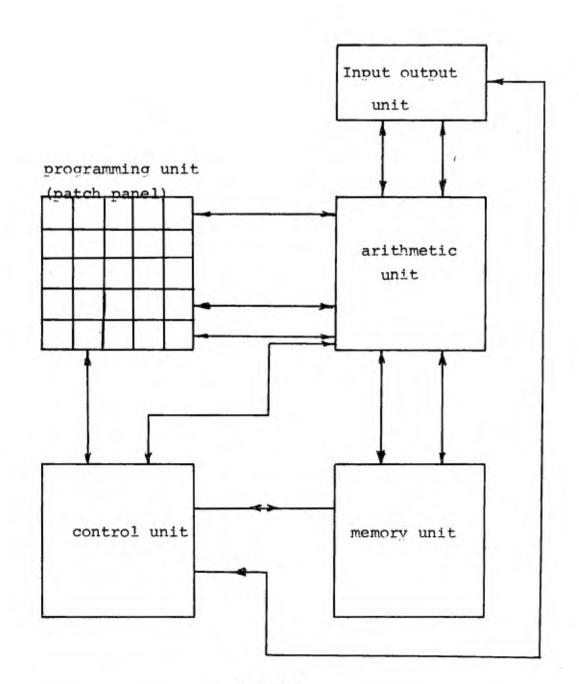
The incremental computer consists of five main units: Arithmetic unit, programming unit, memory unit, control unit and input output unit.

The arithmetic unit is an electronic device which is able to carry out the integration based on unitary or multiple incremental computation, as well as the summation and multiplication of the functions. In addition, it operate the basic arithmetic operations, such as additions, substractions, multiplications and divisions, as general purpose digital computer. The multiplication is done in one time of addition with a special algorithm.

The programming unit is based on the new system of transmitting the information between the integrators. The transmission of data from the output of the integrators to the input of a desired integrator is performed on the patch pannel as the analogue computer. The transmissions data are carried out by one cable for unitary and multiple incremental computations on the patch pannel. Therefore, there is no need of storing the programme instruction in the memory, and the programme is easily checked by visual observations.

The memory unit is used to contain the initial numerical data, calculated intermediate results, the final results, as well as the sets of instruction for arithmetic operations.

The control unit is also indispensable for the automatic functioning of the computers. In general, it coordinates the operation of the other four main parts. The block diagram of incremental computer is shown in figure (6-I)





#### 6 2. Arithmetic unit.

In serial incremental computer, the operations are proceeded sequentially in a common arithmetic unit.

The arithmetic unit is composed of six registers A, B, C, D, R, S, a multiplier M and some one bit adders, as they are shown in fig.(6-2). and(6-3). The logical design of this unit is described by building up the complete schematic from the basic logical element.

The principale operations are as following:

6 2.1. The incremental operations.

- 6.2.1.1. The integration operation based on the unitary incremental computation, by the rectangular method.
- 6 2.1.2. Integration operation based on the unitary incremental computation with trapezoidal method.
- 6 2.1.3. Integration operation based on the multiple incremental computation.

6.2.1.4. New system of multiplication of two functions.

6 2.1.5. Summation of the functions or digital servo.

6.2.1.6. Function generator.

6.2.2. Numerical operation.

6 2.2.1. Addition and substraction of two 10 bit numbers A, B. (B): = (B) ± (A)

6.2.2.2. Addition and substraction of two 20 bits number:

 $(S,R) := (S,R) \pm (A,B)$ 

6 2.2.3. Addition and substraction of three numbers A, B and R in one time of addition. (R): = (R) + (B±A).

6.2.2.5. Division.

6.2.3. Logical and other auxiliary operations.

We explain now, the logic flow of each operation in more details.

6.2.1.1. Integration operation, based on the unitary incremental computation, by the rectangular method.

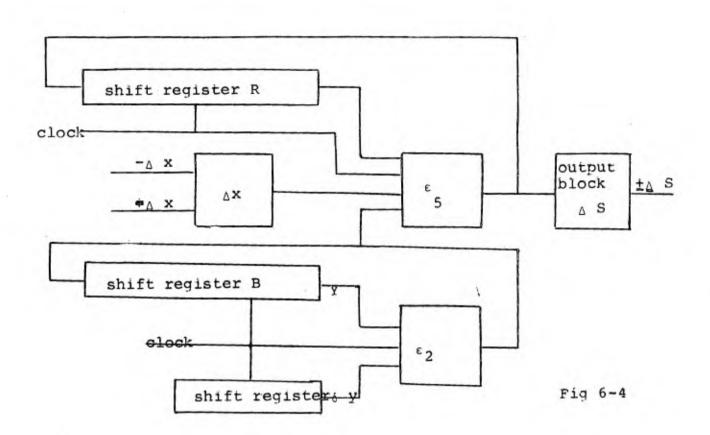
The basic operation is formed by using three registers, by, B,

and R. As it was seen, the algorithm of this method is:

$$\begin{cases} y_{iQ} = y_{(i-1)Q} + \delta_{iQ} y \\ \\ \delta_{iQ} s = \frac{1}{2^n} \circ [y_{iQ} \circ \delta_{iQ} x + s_{o(i-1)} - s_{oi}] \end{cases}$$

The value  $\delta_{i}y$  comes from programming unit and is put in the register  $\delta y$ . The values of the functions  $y_{(i-1)Q}$ , and the rest of integral  $s_{O(i-1)}$  which arrives from the memory, are put in the registers B and R. The first adding operation of the above equation is done by the registers B,  $\delta_{iQ}y$  and the adder  $\epsilon_{2}$ . The result is put in the register B. The output  $y_{iQ}$  of the adder  $\epsilon_{2}$  is sumed by the rest of integral  $s_{O(i-1)}$  from register R in the adder  $\epsilon_{5}$  at the same time that the first operation is performed. If there is an overflow, the output block  $\Delta s$  gives the signe and the value of  $\Delta s$  which is stored in the increment memory of programming units for applying as the input of other integrators or output device: Fig. (6 - 4)

The communication system between the integrators, is the ternary system; that means an overflow  $\Delta s$  can have one of the three values  $\pm 1$  or 0. The actual significance of the "one" is of course, determined by the scale given to it during the programming of the machine. The ternary communication systems are well known and explained in the litrateaure, we explain only the algorithm by flow diagram. Fig. (6-5)



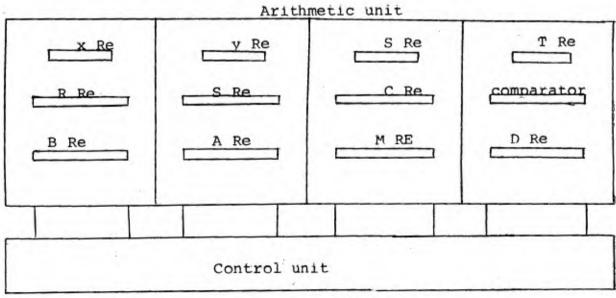


Fig 6 -3

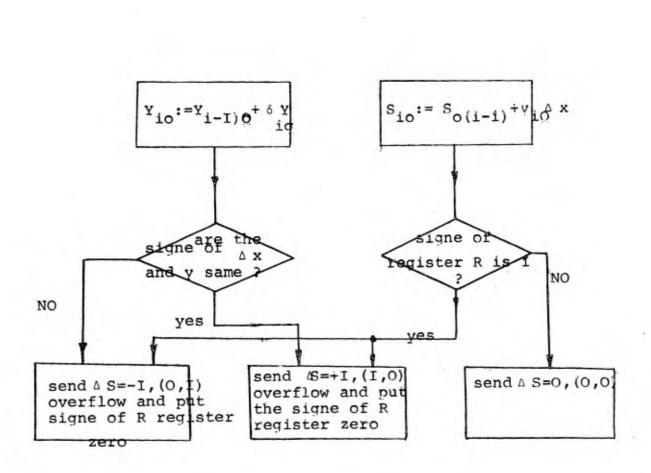


fig 6-5

6.2.1.2. Integration operation based on the unitary incremental computation with the trapezoidal method of integration.

The algorithms of trapezoidal method of integration is:

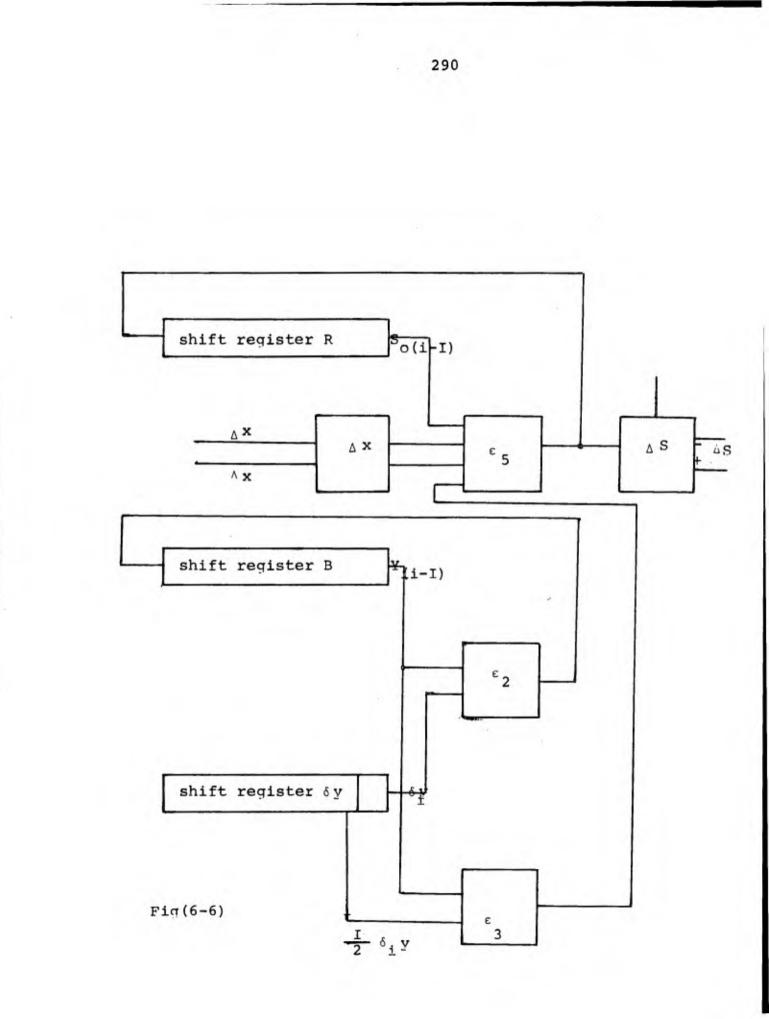
 $+ s_{o(i-1)} - s_{oi}$ 

$$\begin{bmatrix} y_{iQ} = y_{(i-1)Q} + \delta_{iQ}y \\ \\ \Delta_{iQ}s = \frac{1}{2} \left[ (y_{(i-1)Q} + \frac{1}{2} \Delta_{iQ}y) + \Delta_{iQ}x + \frac{1}{2} \right] \end{bmatrix}$$

The logic flow of the system is shown in figure (6-6). The first operation of the above equation is performed by the registers  $\delta y$ , B, and the adder  $_2$ . The adder  $\varepsilon_3$  provide the sum  $(y_{i-1} + \frac{1}{2} \Delta_{iQ} y)$ . This sum is added to  $(\Delta x = +1)$  or substracted from  $(\Delta x = -1)$ , inefected  $(\Delta x = 0)$  the rest of integral  $s_{o(i-1)}$ by the adder  $\varepsilon_5$ .

The three addition operations are performed simultaneously. Therefore, it is very rapid and economical system.

The output overflow ± As will produce with the same algorithms, as rectangular method.



6.2.1.3. Integration based on multiple incremental computation.

As it was discussed earlier, by increasing the step of integration  $\Delta x$  in D<sub>0</sub>D<sub>0</sub>A<sub>0</sub> to  $\delta x = 2^{r} \circ \Delta x$ , (h>r>0) in multiple incremental computer, the speed of integration is increased by the factor of  $2^{r}$ .

Assume y is chosen in such a way that fulfil the following requirement:

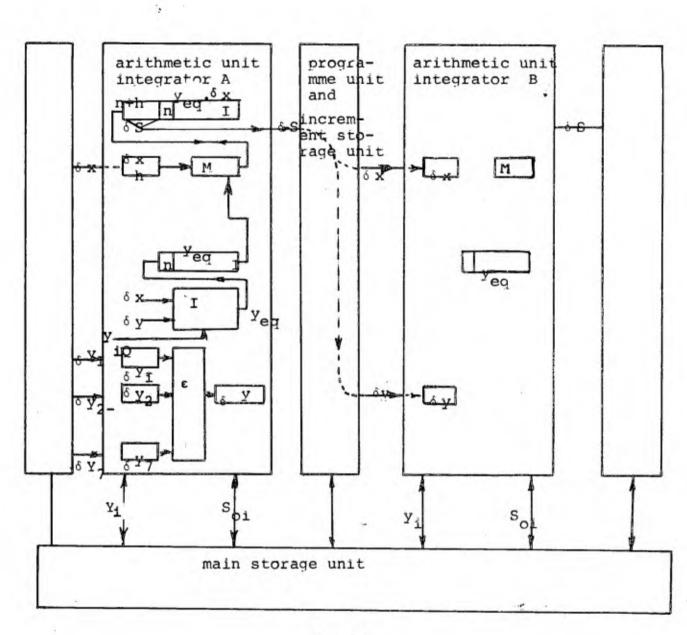
$$\delta_{i}s_{Q}^{x} = Y_{eq} \circ \delta_{iQ}x$$

where

 $y_{eq} = f [y_{iQ}, \delta_{iQ}x, \delta_{iQ}y, \dots]$ 

Therefore, if we put the value of  $y_{eq}$  in register  $y_{eq}$ (fig6-7) and multiply by the  $\delta_{iQ}x$  in multiplier M, then the value of integral will be  $y_{eq} \circ \delta_{iQ}x$  in S register. If the number of bit in  $y_{eq}$  register is n, in  $\delta x$  register is h, then the number of bits of S register will be n + h. The rounded off value of integral  $\delta_i s_{QM}^{x}$  is h most dignificant bits of S register from n + 1 to n + h. This value of integral is transmettid to the increment storage of programming unit as the output of integrator. The n less significant bits of S register goes to the memory for adding to the value of  $y_{eq} \delta_{(i+1)O}x$ , in the next iteration (i+1).

The value of  $y_{eq}$  is formed from  $\delta x$ ,  $\delta y$  which comes iQ iQ





from programming unit. The value of  $\delta_{1Q}y$  is the sum of  $\delta_{1}y$ ,  $\delta_{2}y$ ,  $\cdots$  $\cdots$ ,  $\gamma_{7}y$ , which comes from programming unit, and is added in the adder  $\varepsilon$ , that result is put in  $\delta y$  register.

We will now explain the operation of arithmetic unit of incremental computer of the Brussel University, which perform the integration on the basis of multiple increment.

The most economical and convenient algorithms of integration for multiple incremental computation as it was seen in chapter  $5_p$ are:

$$\begin{cases} \delta_{1Q} s^{x} = [y_{1Q} \circ \delta_{1Q} x + \frac{1}{2} \delta_{1Q} y \circ \delta_{1Q} x + \\ + \frac{1}{12} (\delta_{1Q} y \circ \delta_{(1-1)Q} x - \delta_{(1-1)Q} y \circ \delta_{1Q} x)] \\ y_{eq} = y_{1Q} + \frac{1}{2} \delta_{1Q} y + \frac{1}{12} (\delta_{1Q} y \frac{\delta_{(1-1)Q} x}{\delta_{1Q} x} - \delta_{(1-1)Q} y) \\ y_{1Q} = y_{(1-1)Q} + \frac{7}{2} (\delta_{1Q} y)_{j} \\ \delta x = 2^{r} \circ \Delta x \\ \delta y = 2^{r} \circ \Delta y \\ \delta s = 2^{r} \circ \Delta s \end{cases}$$

As the realization of terms  $\frac{1}{12} (\delta_{iQ} y \circ \delta_{(i-1)Q} x - \delta_{(i-1)Q} y \circ \delta_{iQ} x)$  need too much equipment for the factor  $\frac{1}{12}$ , so with a good approximation we can replace this term by  $\frac{1}{16} (\delta_{iQ} y \circ \delta_{(i-1)Q} x - \delta_{(i-1)Q} x)$  which is easy to realize it. Therefore,  $\delta_{iQ} y$  will be:

$$\delta_{iQ}s = [y_{iQ} \circ \delta_{iQ}x + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}x + \frac{1}{2} \delta_{iQ}y \circ \delta_{iQ}x + \frac{1}{16} (\delta_{iQ}y \circ \delta_{iQ}y \circ \delta_{iQ}x - \delta_{iQ}y \circ \delta_{iQ}x)]$$

The logic flow of the integration operation is shown in figure (6-8).

The values  $(\delta_{iQ}y)_1$ ,  $(\delta_{iQ}y)_2$ ,  $(\delta_{iQ}y)_3$ ,  $(\delta_{iQ}y)_4$ ,  $(\delta_{iQ}y)_5$ ,  $(\delta_{iQ}y)_6$ ,  $(\delta_{iQ}y)_7$ , and  $\delta x$  come from the programming unit, and are put in the 4 bit shift registers  $\delta_1 y$ ,  $\delta_2 y$ , ....  $\delta_7 y$ . The increments  $(\delta_{iQ}y)_1$ ,  $(\delta_{iQ}y)_2$ , ....,  $(\delta_{iQ}y)_7$ , are summed by the adders  $\varepsilon_6$ ,  $\varepsilon_7$ ,  $\varepsilon_8$ ,  $\varepsilon_9$ ,  $\varepsilon_{10}$ ,  $\varepsilon_{11}$  and the results are put in the  $\delta y$  register with 7 bits. The value of  $y_{iQ}$  is get from the output of the adder  $\varepsilon_2$ and is put in the register B. The values of  $y_{iQ} + \frac{1}{2} \delta_{iQ}y - \frac{1}{16} \delta_{(i-1)Q}y$  is found from the output of the adder  $\varepsilon_1$ , and is multiplied in multiplier M, by the value of  $\delta_{iQ}x$ . The output of the multiplier M is added to the value  $\frac{1}{16} \delta_{(i-1)Q}x \circ \delta_{iQ}y$ , which is the output of the multiplier M<sub>2</sub>, and gives at its output the

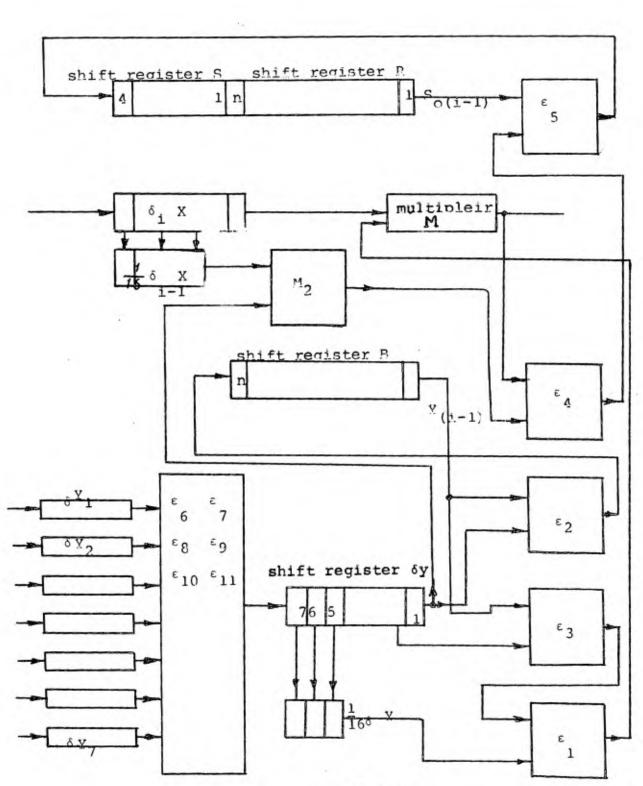


Fig 6-8

$$y_{iQ} \delta_{iQ}^{x} + \frac{1}{2} \delta_{iQ}^{x} \delta_{iQ}^{y} + \frac{1}{16} (\delta_{iQ}^{y} \delta_{(i-1)Q}^{x} - \delta_{iQ}^{y})$$

 $-\delta_{(i-1)Q} \circ \delta_{iQ}$ . This sum is entered in the adder  $\epsilon_5$  and is summed with the rest of integral  $s_{O(i-1)}$  of the preceeding iteration (i-1). The output of the adder  $\epsilon_5$  is:

$$[y_{1Q} \circ \delta_{1Q} x + \frac{1}{2} \delta_{1Q} y \circ \delta_{1Q} x + \frac{1}{16} (\delta_{1Q} y \circ \delta_{(1-1)Q} x -$$

$$= \delta(i-1)Q^{Y} \delta_{iQ}x] + s_{o(i-1)}$$

As the value of y is put in 10 bits B register, and the value of  $\delta_{iQ}x$  in four bits  $\delta x$  register, the value of  $y_{iQ} \circ \delta_{iQ}x$ and the above bracket will have maximum 14 bits. This value is put in the 14 bits register (S,R), (10 bits of register R and 4 bits of register S). Four most significant bits of the register (S,R), are taken as the output  $\delta_{i}s_{QM}^{x}$ , which is the rounded off value of  $\delta_{i}s_{Q}^{x}$ . The increment of integration  $\delta_{i}s_{QM}^{x}$  is sent to the increment memory in programming unit. The less significant bit (register R) is the new rest of the integral  $s_{oi}$ , which is sent to the memory with the value of  $y_{i0}$  for the next operation.

### 6,2.1.4. New system of multiplication of two functions.

The multiplication of two functions x and y was achieved normally in a differential form.

$$d(x, y) = x^{\circ} dy + y^{\circ} dx$$
(1)

Thus, a minimum of two integrators and an adder (fig.6-9) was required.

The exact value of  $\Delta(x, y)$  is:

$$(\mathbf{x}_{i} + \boldsymbol{\Delta}_{i}\mathbf{x}) \quad (\mathbf{y}_{i} + \boldsymbol{\Delta}_{i}\mathbf{y}) = \mathbf{x}_{i}\mathbf{y}_{i} + \mathbf{x}_{i} \circ \boldsymbol{\Delta}_{i}\mathbf{y} + \mathbf{y}_{i} \circ \boldsymbol{\Delta}_{i}\mathbf{x} + \boldsymbol{\Delta}_{i}\mathbf{x} \quad \stackrel{(2)}{\boldsymbol{\Delta}_{i}\mathbf{y}}$$
$$\Delta(\mathbf{x}_{i} \circ \mathbf{y}_{i}) = \mathbf{x}_{i} \circ \boldsymbol{\Delta}_{i}\mathbf{x} + \mathbf{y}_{i} \circ \boldsymbol{\Delta}_{i}\mathbf{x} + \boldsymbol{\Delta}_{i}\mathbf{y} \circ \boldsymbol{\Delta}_{i}\mathbf{x} \quad (3)$$

Normally the third term in equation (3) was neglected and the equation(1) was used with three integrators, with the error of  $\Delta_i x$ ,  $\Delta_i y_c$ 

But a new method in just calculating the sum of the three terms in the equation (3) :s:

$$\Delta(\mathbf{x} \cdot \mathbf{y}) = \mathbf{x}_{i}^{\circ} \Delta \mathbf{y} + \mathbf{y}_{i+1}^{\circ} \Delta \mathbf{x}$$
(4)

Therefore, we can calculate the equation (4) in arithmetic unit, and transmitting the increment of function  $\Delta(x \cdot y)$  as it was done in the integration, with the ternary communication system. In this system we can build the multiplier block in 'ncremental computer, with the inputs  $\Delta x$ ,  $\Delta y$  and the output  $\Delta(x \cdot y)$ , exactly with the communication system of integrators, and using one block instead of three integrators, with higher accuracy. The logic flow of this operation is shown in fig. (6-10)

There are five multiplier of function in the incremental computer of the industrial electronic laboratory of the Brussel University, which are designed by the author. The transmission of informations in input and output of the multiplier is done by only one cable on the patch pannel, as it was the case of the integration.

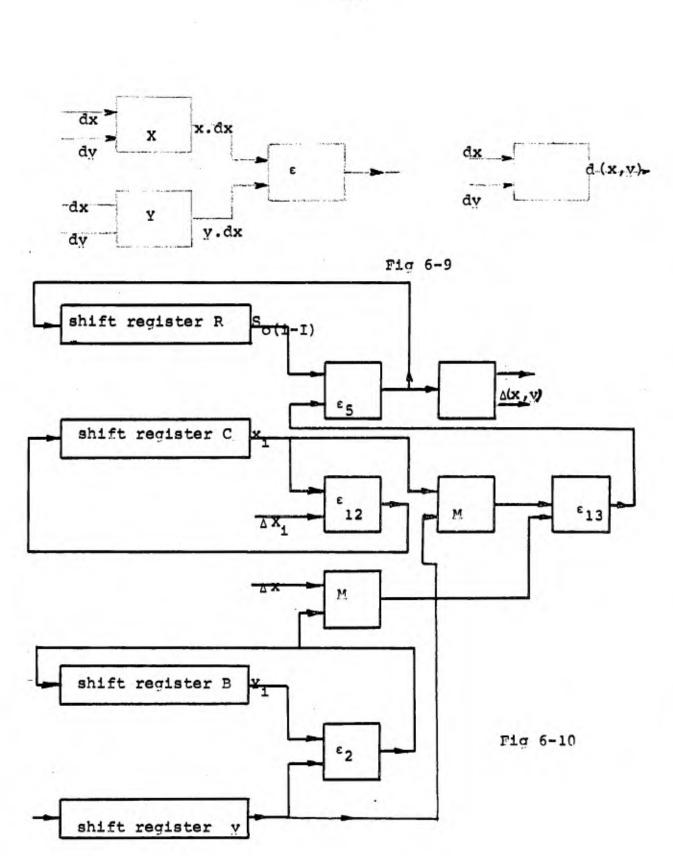
# 62.1.5. Summation of the functions or digital servo.

Though a separate accumulator or the Y accumulator of an integrator can accept two or more incremental inputs, and accumulate their sum, it cannot generate a rate equal to the sum of the input rates. The addition of rates may be performed by using an integrator programmed to function as a digital servo.

Considering first how a digital servo can produce an incremental output equal to the sum of two inputs.

Since its operation has been governed by the number system used, the description will be related specifically to the number system usually employed in the integrators with bineary communications.

This number system, shown in figure (6-ID, is described as

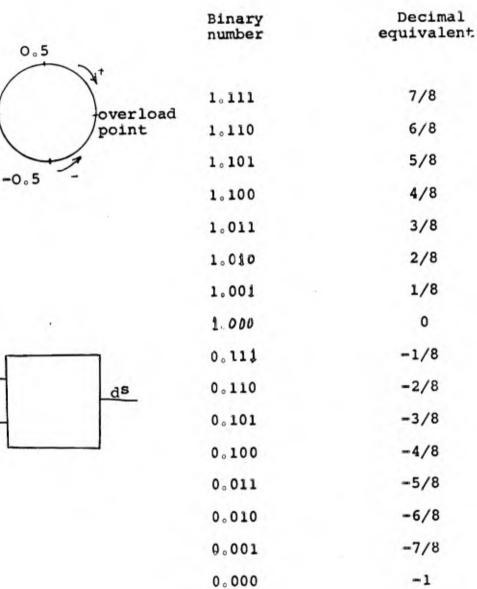


circular, because, when an increment is added to the representation of the maximum positive number,  $1 - 2^{-n}$ , the result is the representation of the maximum negative number -1, and conversely, substracting a single increment from -1, produces  $1 - 2^{-n}$ .

In servo integrator we put the maximum positive number of  $1 - 2^{-n}$  in Y register of the integrator, and the independent variable is time. Under these conditions, the integrator will produce a 1 at its output during each iteration period. The sequence of 1's, thus generated, represents the the maximum positive output rate of the machine. If a single bit is added in the least significant bit, the value of the integral will change from  $1 - 2^{-n}$  to -1 and the integrator will then generate a sequence of zeros, the maximum negative rate.

The initial value of y register is put to  $1 - 2^{-n}$  (f.1111...1). If the output ds is feedback with the negative signe to the input dy, then by applying the quantities e in the other input of dy, the first output signal will be a positive rate (+1), and the value of y register becomes  $1 - 2^{-n} + e$ . When fed back, during the next iteration period it change from  $1 - 2^{-n} + e$  to  $1 - 2^{-n} + e$ e - 1. As a result the next output of the integrator will be a negative increment, which feeds back with opposite signe converting the value of integrand to  $1 - 2^{-n} + e$ , but as this value is a negative number, it gives at the output a negative increment, and





zero

dt

dy

Fig 6-11

feeds back by (+1), so the new value of integrand will be  $1 - 2^{-n} + e + 1$ . This process continues until the value of Y register becomes  $1 - 2^{-n}$ , as its initial value, and the number of pulses at the output of the system becomes to e.

This system has the application when we want to send some pulses equal to a number e. The servo system can be used in feedback control systems; for generating a number of positive or negative pulses, depending on the application.

There are five servo systems with seven dy inputs , four outputs +  $\Delta$ s and - $\Delta$ S in the incremental computer of the industrial electronic laboratory. The systems of communication are bineary systems, and the interconnections between them and the integrators are done by one cable on the patch pannel.

## 6.2.1.6. Function generator.

In analogue computation, it is possible to generate any function with the linear method of interpolation, by the diode resistance function generator.

The variation of resistance R and the number of diode in the circuit can generate any function by the linear interpolation formula.

The same operation can be done by the increment principale by the variation of numerical value W and a (fig.6-3)

In this aim, if we use an integrator as a multiplier, the content of Y register of this integrator is a which comes from the memory. The value of W is put in the W register of the memory. The output dz of the integrator is:

```
dz = a \circ dx
z = \int dz
```

or

= a ' [dx = a ' x

The integrator continues to perform the integration operation for each dx input; until the number of dx input pulses which are count by the counter X, becomes equal to W. At this moment, the comparator gives the signal of the end of operation. The reversible counter (C) count the dz pulses, so its content will be  $Z = a \circ W$  at that instant Fig. (6 - 14)

By choosing the value of a in the Y register of integrator and w in the W register, it is possible to generate any linear curve with any slope (a) and bia; (W).

So, by generating the curve oA, AB, BC,.... one after the other, we can produce the curve o A B C D ..... M N.Fig (6-13

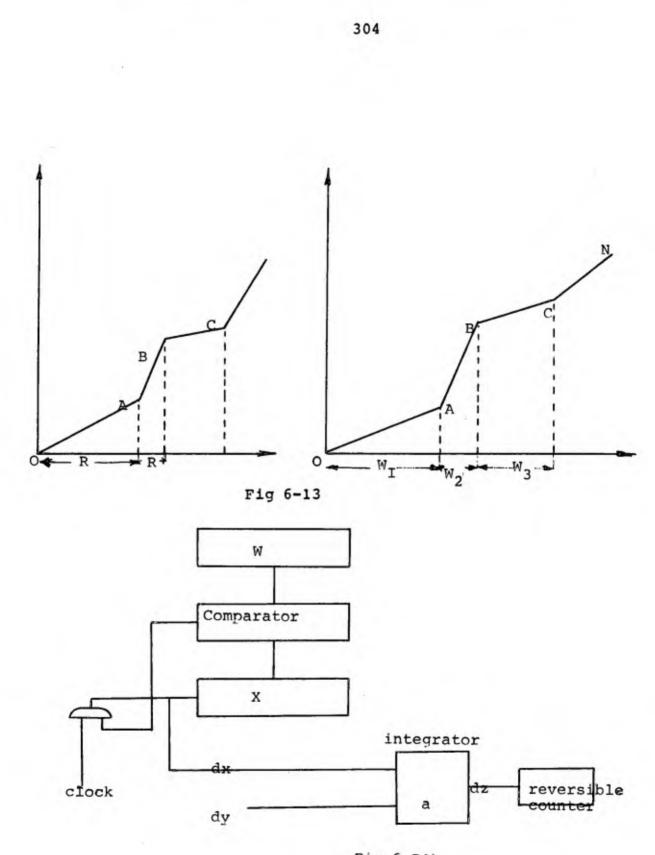


Fig 6-14)

6,2.2. Numerical operations.

The additions and substractions are done bits adders and the registers A, B, R, S,.

6.2.2.1. Addition and substraction of two numbers A, B, (B): = (B) ± (A).

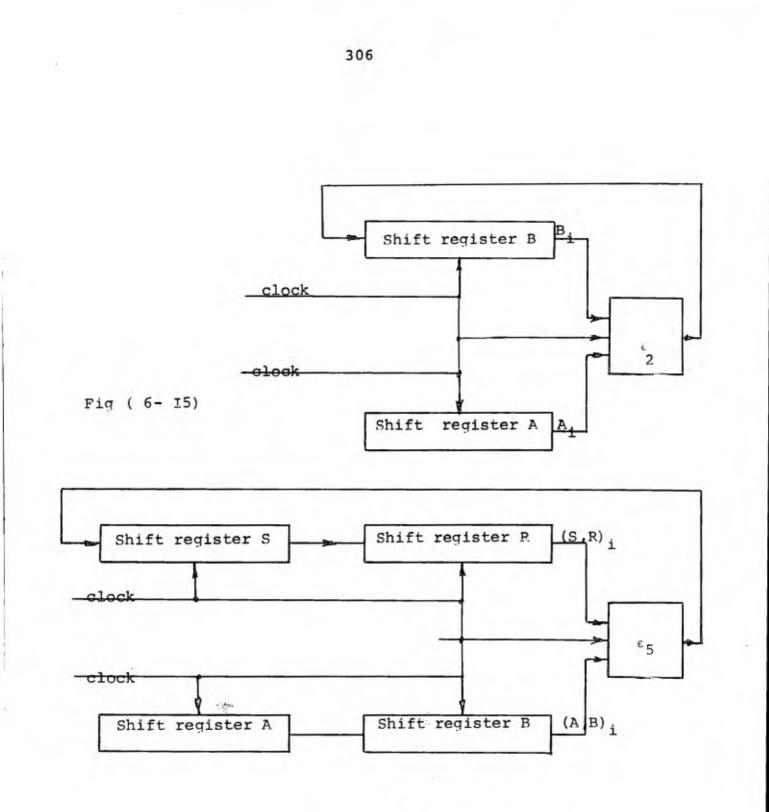
For this operation the shift registers A, B and the adder  $\varepsilon_2$  are used. The results of operation are put in register B, as it is shown in figure (6 - 5)

6 2.2.2. Addition and substraction of two 20 bits number, (S,R): = (S,R) ± (A,B).

The addition and substraction of two numbers (S,R) and (A,B) are done in adder  $\varepsilon_5$ , and the results are put in register (S,R), as it is shown in figure (6 - 6)

6.2.2.3. Addition and substraction of three numbers A, B and R, (R): = (R)  $\pm$  (A  $\pm$  B).

The addition and substraction of three numbers R, A, B,



Fig(6-16)

are done by the registers A, B, R and the adders  $\epsilon_2$ ,  $\epsilon_5$ . The sum of these three numbers are done in one time of addition and the results are put in register R, as it is shown in figure (6-17)

6 2.2.4. Multiplication of two numbers in one time of addition. (S,R): = (B) ° (C) or (S,R): = (B ± A) ° (C).

The multiplication of two numbers  $\vec{B}$  and C or the algebraic sum of (B  $\pm$  A) to (C) is done with a special algorithm which is a kind of serial and parallel multiplication. The time of multiplication is equal to one time of addition.

If  $C_1, C_2, \ldots, C_{10}$  are the outputs of C register bits, then the multiplication algorithm is:

(B) • (C) = B • 2<sup>9</sup> • C<sub>10</sub> + B • 2<sup>8</sup> • C<sub>9</sub> + B • 2<sup>7</sup> • C<sub>8</sub> + • • • • + B • C<sub>1</sub>

The logic flow is shown in figure ( $\epsilon$  })

The multiplier M has 9 adders and one 1 bit memory for 10 bits C register. The output of one bit memory is  $2^9 \cdot C_{10} \cdot B_i$ , the output of  $\varepsilon_9$  is  $2^8 \cdot C_9 \cdot B_i$ , The output of  $\varepsilon_8$  is  $2^7 \cdot C_8 \cdot B_i$  and so on. Therefore, the output of each adder gives one term of above algorithm \*

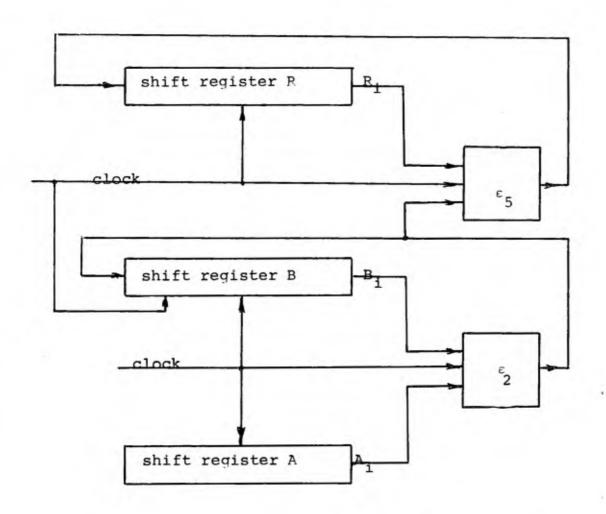


Fig (6-17)

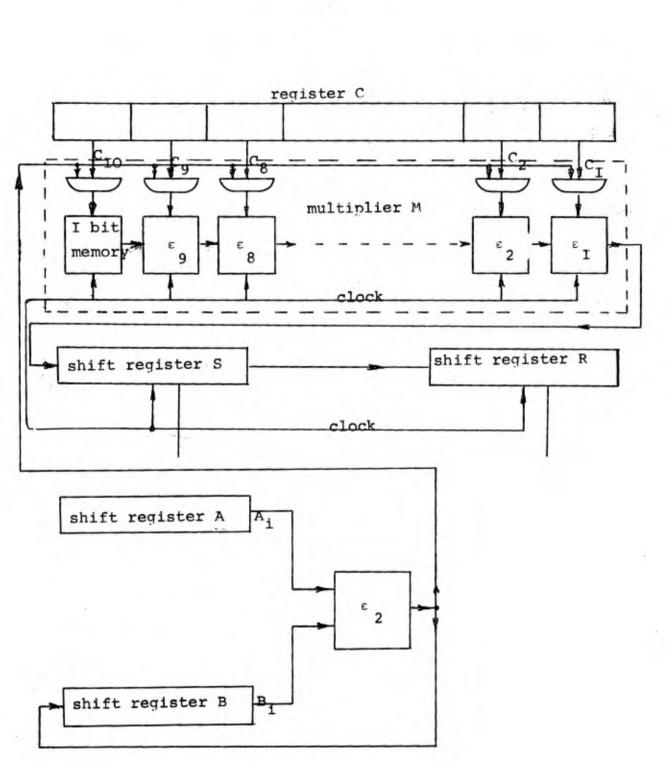


Fig (6-18)

As it is seen, the value of  $B_i$  or  $(B_{i+1})$ : =  $(B_i) \pm (A_i)$  arrives in serial form at the input of multiplier, and in the multiplier this value is multiplied by the value of C register in the parallel form. The output of multiplier is the output of adder  $\varepsilon_1$  which is stored in the register (S, R).

The first advantage of this multiplier is that, it does the multiplication in one-time of addition, and its input  $B_1$ can be the output of any adder or logical function. For instance here, the output of  $\varepsilon_2$  is applied for the multiplication of  $B_1 \cdot C$  or  $(B_1 \pm A_1) \cdot (C)$ . In the multiple incremental computation, the output of adder  $\Sigma$  is applied to its input for the operation:

$$(\mathbf{y}_{\mathbf{i}} + \frac{1}{2} \delta_{\mathbf{i}} \mathbf{y} - \frac{1}{16} \delta_{\mathbf{i}-1} \mathbf{y}) \circ \delta_{\mathbf{i}} \mathbf{x}$$

The other advantage is that, the amount of equipment depends on the number of bits in C register. For instance, if we want to multiply the four bits of C register to ten bits of B register, then we need only 3 adders  $\varepsilon_1$ ,  $\varepsilon_2$  and  $\varepsilon_3$  instead of 9 adders.

6.2.2.5. Division.

If A is dividand, D is the divisor, q is the quotient and

r is the rest of the division. Then, the division equation is:

$$\frac{A}{D} = q + \frac{r}{D}$$

In order to do the division of the number A over D, the number A should be put-in-the register (S,R), the number D in the register A.

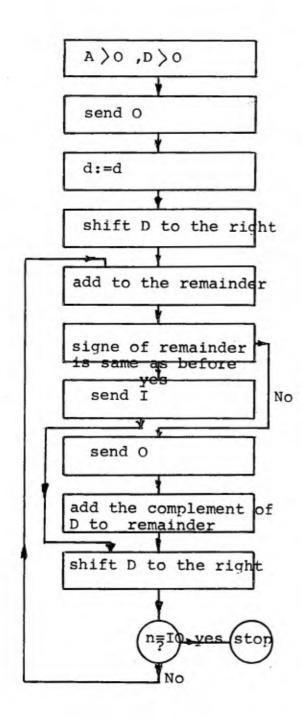
After the division operation, the quotient q comes in the register C, and the rest of integral in the register (S,R). The algorithm of the operation is shown in the flow diagram(6-19)

When  $\begin{bmatrix} A > o \text{ or } A < o \end{bmatrix}$ , we should add one bit to the result D < o D > o

quotient q Therefore, another adder  $\epsilon_{l3}$  is necessary. The control unit gives all the control signal and clock pulses which are necessary for the division operation.

#### 6 2.3. Logical and other auxilary operations.

Four 10 bits shift registers A, B, S, and R or two 20 bits shift registers (A, B) and (S, R), can be used independently, as the shift registers for any purpose of calculation, as they are



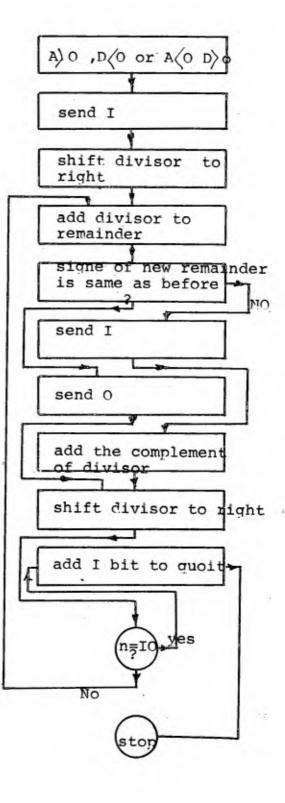


Fig 6-19

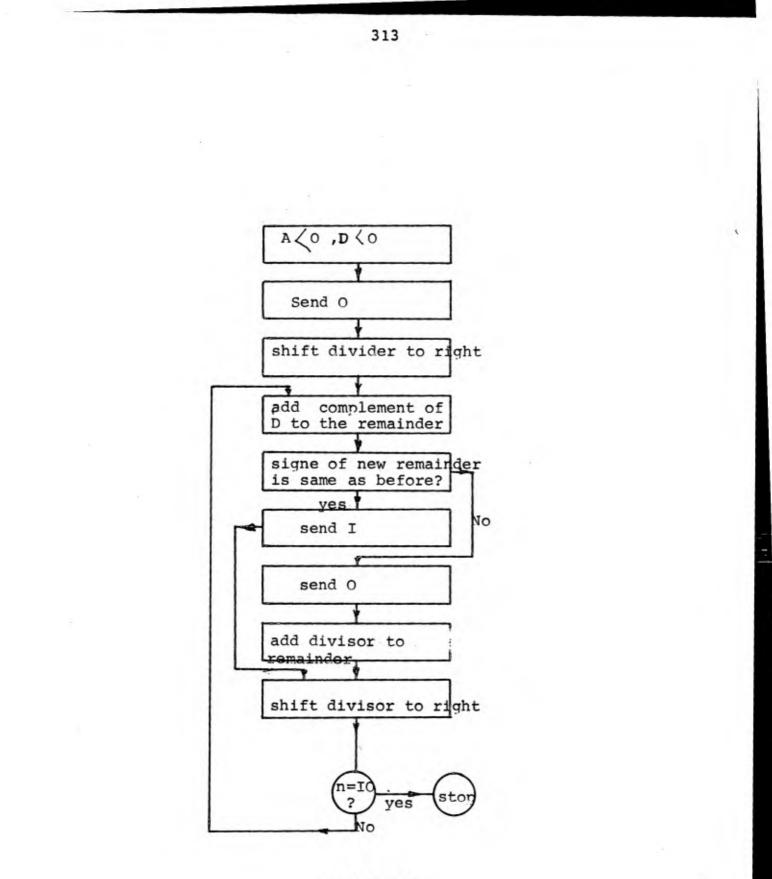
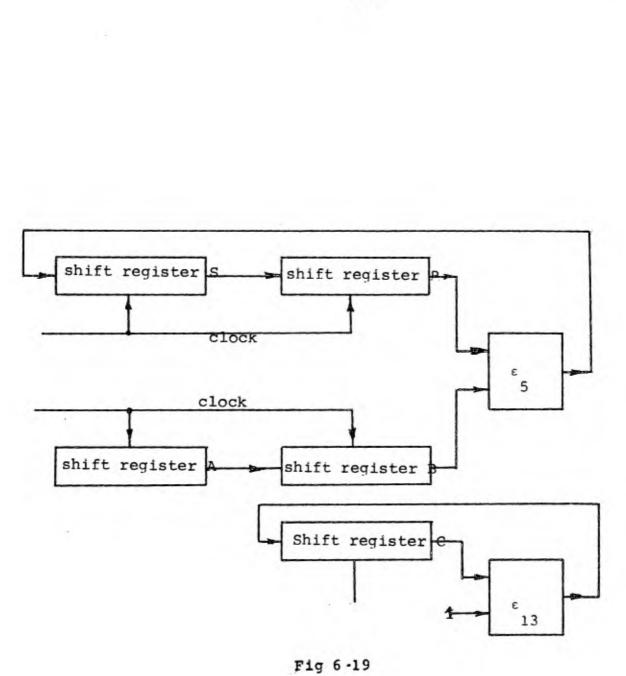


Fig 6-19



315

shown in figure (6-20).

There is a reversible counter which is called register D. The flipflops which are used in this counter are the T type flipflops. The input equations of each bit are:

	$Y_1 = C + D$
	$Y_2 = Y_1^\circ C + Y_1^\circ O$
	$Y_3 = Y_1^{\circ} Y_2^{\circ} C + Y_1^{\circ} Y_2^{\circ} D$
	$Y_{4} = Y_{1}^{\circ} Y_{2}^{\circ} Y_{3}^{\circ} C + Y_{1}^{\circ} Y_{2}^{\circ} Y_{3}^{\circ} D$
$Y_{10} = Y_1 \circ Y_2$	°Y°Y°Y°Y°Y°Y°C
+ Y' * Y'	° Y' ° D
$\mathbf{Y}_{11} = \mathbf{Y}_1 \circ \mathbf{Y}_2$	° Y <sub>3</sub> ° Y <sub>4</sub> ° Y <sub>5</sub> ° Y <sub>6</sub> ° Y <sub>7</sub> ° Y <sub>8</sub> ° Y <sub>9</sub> ° Y <sub>10</sub> ° C +
+ Y' * Y'	* Y <sup>1</sup> <sub>3</sub> * Y <sup>1</sup> <sub>4</sub> * Y <sup>1</sup> <sub>5</sub> * Y <sup>1</sup> <sub>6</sub> * Y <sup>1</sup> <sub>7</sub> * Y <sup>1</sup> <sub>8</sub> * Y <sup>1</sup> <sub>9</sub> * Y <sup>1</sup> <sub>10</sub> * D

There is a comperator which indicate whether two numbers C and D of ten bits are equal or not. The logical equation is:

 $\mathbf{E}_{\mathbf{i}} = \mathbf{C}_{\mathbf{i}} \circ \mathbf{D}_{\mathbf{i}} \circ \mathbf{E}_{\mathbf{i+1}} + \mathbf{C}_{\mathbf{i}} \circ \mathbf{D}_{\mathbf{i}} \circ \mathbf{E}_{\mathbf{i+1}}$ 

There is also a simulator for various logical problem.

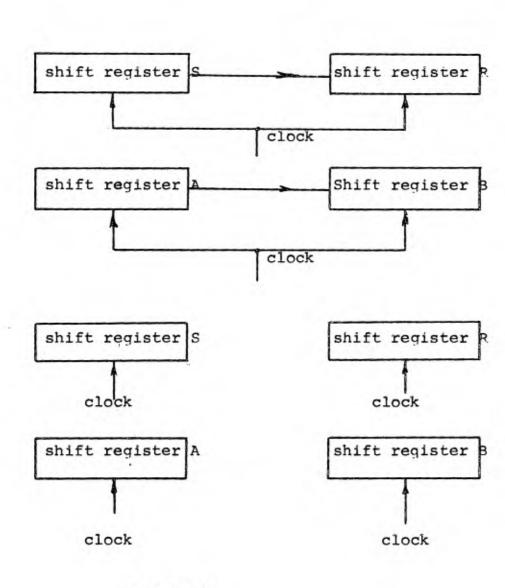


Fig 6-20)

#### 6 3. A new concept for programming unit in incremental computer.

In a serial unitary incremental computer, the programme of a problem is generally stored in the memory, like in the case of serial D.D.A.

In parallel D.D.A., a patch panel used to interconnect the integrators by two leads for one bit unitary increment plus signe.

The new programming unit, use a patch panel for serial and parallel machines, which need only one lead for transmitting the information between the integrators.

The main advantage of this system is that, there is no need of storing the programme instructions in the memory, which need too much memory storage and operation time. Therefore, the programme of a problem is realized on the patch panel, exactly as an analog computer, with only one lead between the positive or negative output, and the dx or dy input of the integrators.

This new system of transmitting the information between the integrators, can be used either for unitary or for multiple increment computation.

The programming unit of the serial incremental computer, which is designed by the author in the industrial electronic laboratory of Brussel university, has two parts.

The former, built on two pannels, offers the possibility of interconnecting sixty integrators working with the unitary increment principle.

The latter, permits of interconnecting ten integrators working with the multiple increment principle. On the same pannel, there are five functions adder and functions multiplier.

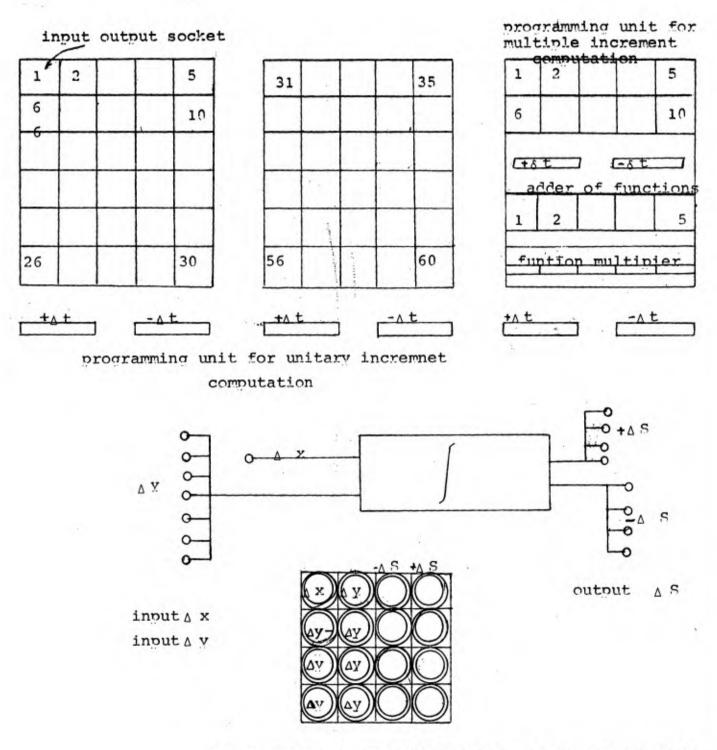
On the patch pannel, each integrator has a block of sixteen sockets. There are one dx input, seven dy inputs, four positive ds and four negative ds outputs. The inputs and outputs of each integrator are shown in figure (6-21)

The algorithms of the system are as following:

The sixty integrators are operated one after another, by the sixty signals  $A_1$  till  $A_{60}$  which arrive from the selector of the control unit.

When the control unit gives the signal A<sub>j</sub>, the following operations take place:

- a) The informations of the integrator j, which were stored in the memory, are transferred to the arithmetic unit.
- b) The programme unit takes the input informations \$x, \$y<sub>1</sub>, \$y<sub>2</sub>,...
   ... \$y<sub>7</sub> from the output of integrators or other devices.



input output in each integrator in programmin unit Fig 6- 21)

- c) The arithmetic unit performs the integration operation and gives the output increment of integral δs.
- d) The increment ôs is stored in the increment memory, which in unitary increment computations is 1 bit memory and in multiple increment computations is h bit memory.
- e) The new values of function y<sub>iQ</sub> and the rest of integral s<sub>io</sub> are stored in the memory of the computer.

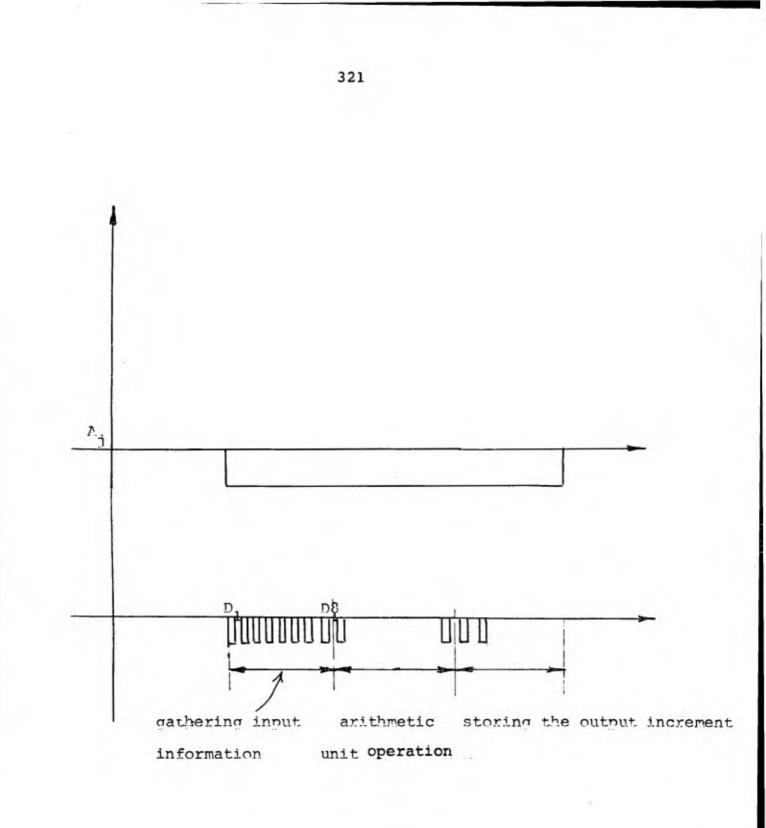
The task of programming unit is first, taking the input information  $\delta x$  and  $\begin{cases} (\delta y) \\ j=1 \end{cases}$  from the output of different integrators and  $j=1 \end{cases}$  sending it to the arithmetic unit. The timing diagram of the arithmetic unit and programming unit is shown in figure (6-22).

The operation of transmitting the information  $\delta x$ ,  $\delta v_1$ ,  $\delta y_2$ , ....  $\delta y_7$  to the input of integrator j at A<sub>j</sub> is done as following:

Let's suppose, the three inputs  $\delta x$ ,  $\delta y_4$ ,  $\delta y_7$ , of integration j are connected to the output of integrator i, p, Q, on the patch pannel, fig (6-23).

At the integrator period  $A_j$ , the integrator j asks the information to its input by sending eight pulses  $A_j D_1$ ,  $A_j D_2$ ,  $A_j D_3$ ,  $A_j D_4$ ,  $A_j D_5$ ,  $A_j D_6$ ,  $A_j D_7$ ,  $A_j D_8$  from its inputs  $\delta x$ ,  $\delta_1 y$ ,  $\delta_2 y$ ,  $\delta_3 y$ ,  $\delta_4 y$ ,  $\delta_5 y$ ,  $\delta_6 y$ ,  $\delta_7 y$ ,

In our example, the integrator j sends the  $A_j D_i$  signal from its input  $\delta x$  by the external lead on the patch pannel. This signal





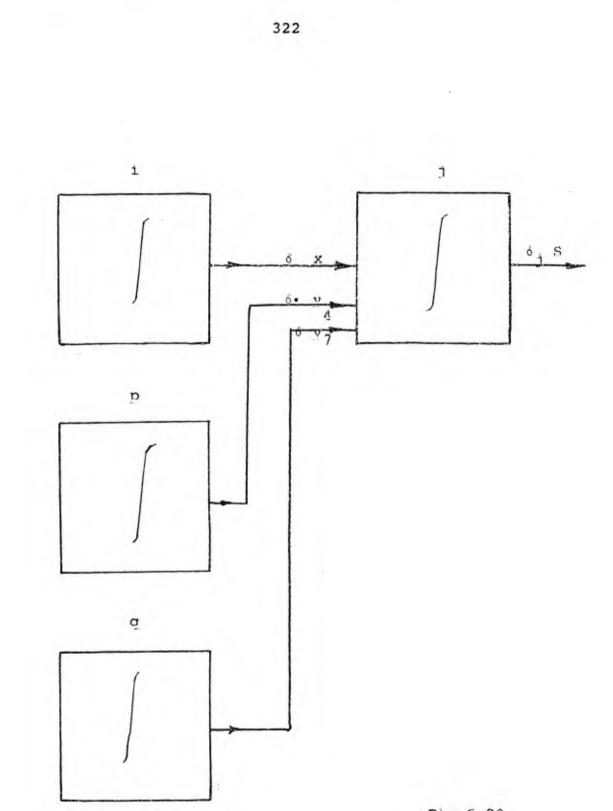


Fig 6-23

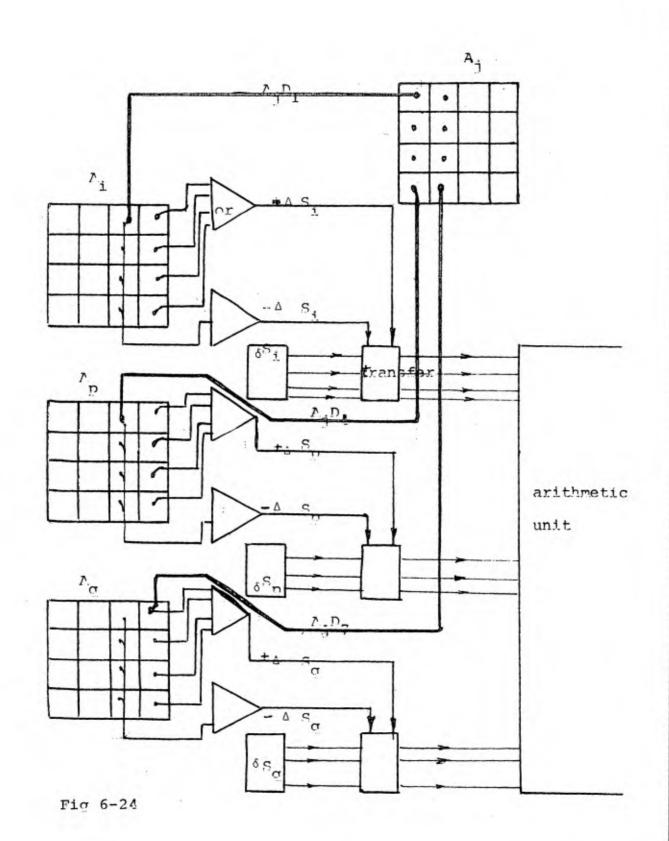
 $A_j D_l$  pass via an O R circuit (+ $\Delta s_i$ ) and gives the order of transferring the value of  $\delta_i s$  to the arithmetic unit fig (6-24).

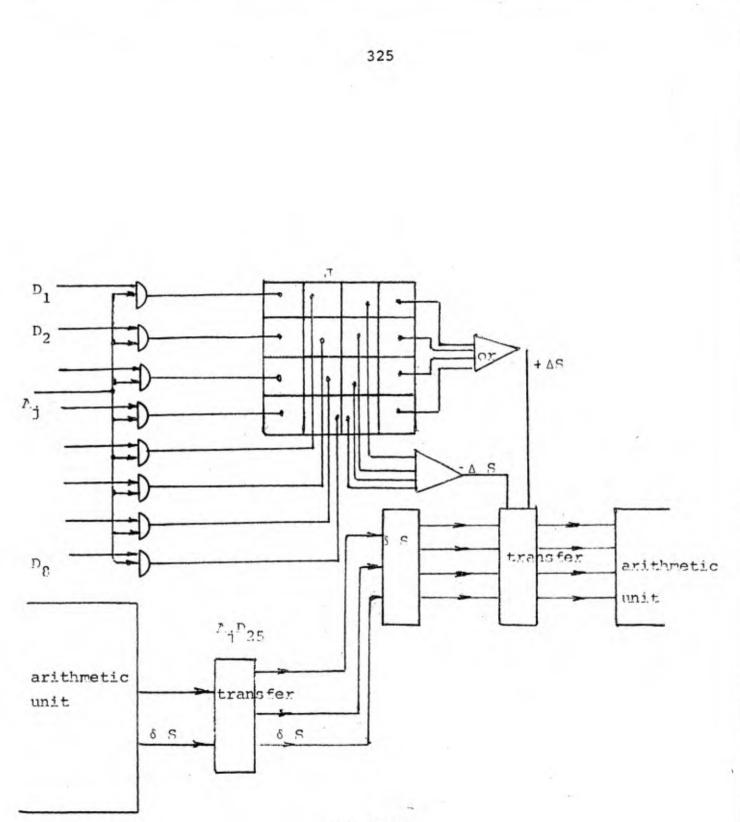
The signals  $A_j D_4$ , and  $A_j D_7$  which are send by the input  $\delta y_3$ and  $\delta y_7$  of the integrator j, transfer the increment  $\delta_p s$  and  $\ell s_q$ to the arithmetic unit.

The internal connection of input output socket of each integrator j, with the increment memory and the arithmetic unit, is shown in figure (6-25)

By this system, when the integrator j begins to operate by sending 8 pulses  $A_j D_j$  to  $A_j D_a$ , via the leads on the patch pannel, it gathers its input information in the arithmetic unit. Then the integral operation is done in the arithmetic unit, at the end, the output  $\delta$ s is send to the memory incremental storage  $\delta_j$ s by the signal  $A_j D_{24}$ .

This system has the great adventages of transmitting the informations between the integrators by only one lead on the patch pannel, for the unitary and multiple increment computation.







## 6.4. Main store of incremental computer.

In this article, a description is given of the main store of the incremental computer, which is designed by the author in the industrial electronic laboratory of the Brussel University.

The basic methematical elements of the computer are a set of sixty integrators which are based on the unitary increment computations. Each of them are represented by a pair of b neary numbers,  $Y_{i}$ and  $s_{oi}$  (i = 1, 2,..., 60). These numbers of ten b neary digits plus signe, are stored on two 11 x 140 magnetic core matrix, from which they are read together in parallel, i.c. The  $Y_{i}$  and  $s_{oi}$  numbers corresponding to one integrator are treated within, the store as a single word of 22 bits. In one complet machine cycle, all sixty words are read in the sequence  $A_{i}, A_{2}, \dots, A_{j}$ . During the period  $A_{j}$ , the number pair  $Y_{i}$  and  $s_{oi}$  are transformed to the arithmetic units. In arithmetic unit, the numbers are operated upon according to a programme, producing two new numbers  $y_{i+1}, \Delta s_{(i+1)o}$ . These numbers are returned to the locations previously occupied by  $Y_{i}$ ,  $s_{oi}$ , in the main store, to be read as a new  $Y_{i}$ ,  $s_{oi}$  during the succeeding machine cycle.

The two memories can also be used completely independent of the incremental computer, they can be programmed and addressed manualy by the signals or by the programme, for any mathematical and logical operation. The values of registers A, B, C, D, R, S of arithmetic unit

can be written in the two memories, and any value which is read in the memory, can be put in the registers A, B, C, D, R, S.

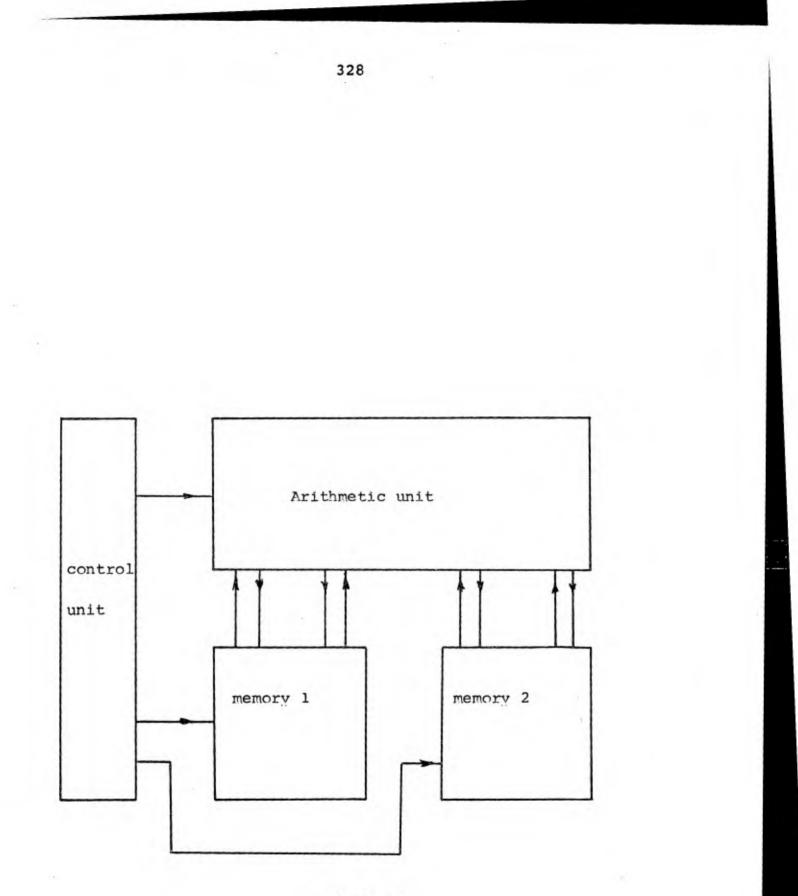
The figure (6-26) shows the interconnections between memory, arithmetic unit and control unit.

Each memory is devided into memory locations. The memory location has the capacity to store one word, consisting of 11 bits. The numbers of bits are same for all locations of the memory. When a word is to be stored, that word is sent to the memory and at the same time an address is given to it. The word is then stored in that address.

This procedure is called writing in the memory. The opposite procedure, is called reading from the memory, again an address is given, whereupon the contents of that address are returned at the arithmetic unit. A store operation in an address automatically involves the erasur of the previous contents of that address.

Information in core storage is stored by using the magnetic properties of ferrite cores. Each ferrite core can store a single bit of information, either a logical 1 or a logical 0.

A ferrite core is a bi-stable storage device. The ferromagnetic properties of the core permit it to be megnetized by applying an internal magnetizing force. Two wires X and Y, carrying electric current, inserted through the core, provide this force. After the core is magnetized, it retains the magnetic flux even though the magnetizing





force is removed. It can be magnetized in either a north-to-south or a south-to-north direction in much same way that a trigger can be set in two states, hence, the term bi-stable. These two states are shown as points A and D in figure (6-27).

Magnetic force H is plotted horizentally, flux density B, is plotted vertically. If a large positive H, represented by the current I is applied, the core retains a flux dendity D, after the magnetizing force has returned to zero. Similary, if a large negative I is applied to the core, the flux follows the path D, E, F. Then the core retains a residual magnetizing equivalent to A after the negative I has been removed. Current greater than I or-I have little effect on the flux density B, because the core is then saturated.

Point D has been arbitary assigned to stand for logical one, and point A for a logical zero.

In figure (6-27) it is evident that  $\frac{1}{2}$  I has little effect on the flux density E of the core. For example, if the core were in the logical one state, point D, and only  $-\frac{1}{2}$  I were applied, the magnetizing force produced would be insufficient to cause the flux density to change beyond point E. The core would therefore not change its bit status from the logica. One to zero as it did when full -I was applied.

The switching system used in core storage, uses this ability of the core to discriminate between  $\frac{1}{2}$  I and full I. Considering the X and Y wires running parallel through the core and each carrying 250 miliamps

of current in the same direction. The resulting force from each current is additive and equivalent to the force produced by one current of 500 milliamps. Assuming that 500 milliamps is the -I required to switch the status of the core, the X and Y wires are each considered to be carrying  $-\frac{1}{2}$  I.

Another wire, called the inhibit wire, passes through the core as shown in fig(6-28). It run parallel to the X wire and, like the X wire, can carry only  $\frac{1}{2}$  I. At the appropriate time, it carries  $\frac{1}{2}$  I in the direction opposite  $\frac{1}{2}$  I in the X wire. The net results are to cancel the effect of the magnetizing force produced by the X wire. The inhibit line is energized when it is not desirable to change the bit status of the core. When -I is applied, it is possible to detect the original state of the core by means of an additional wire, through the core called the sense wire. If the core is in state D when -I is applied, the change of flux, from point D to A, induces a voltage in the sense wire. This voltage pulse indicates that the core contained a one. If the core is at point A when the -I is applied, there is no flux change and no voltage is induced in the sense wire. This absence of induced voltage indicates that the core contained a zero. The core planes with sense amplifies, inhibits drivers and switches are shown in fig(6-29).

The figure (6-29) refers to the 11 planes used for 11 bits of memory. It shows that one X drive line goes through 14 cores in a given row of a plane, and continues through the corresponding row of 14 cores in each of the other planes. In a 10 x 14 core array, one X drive line

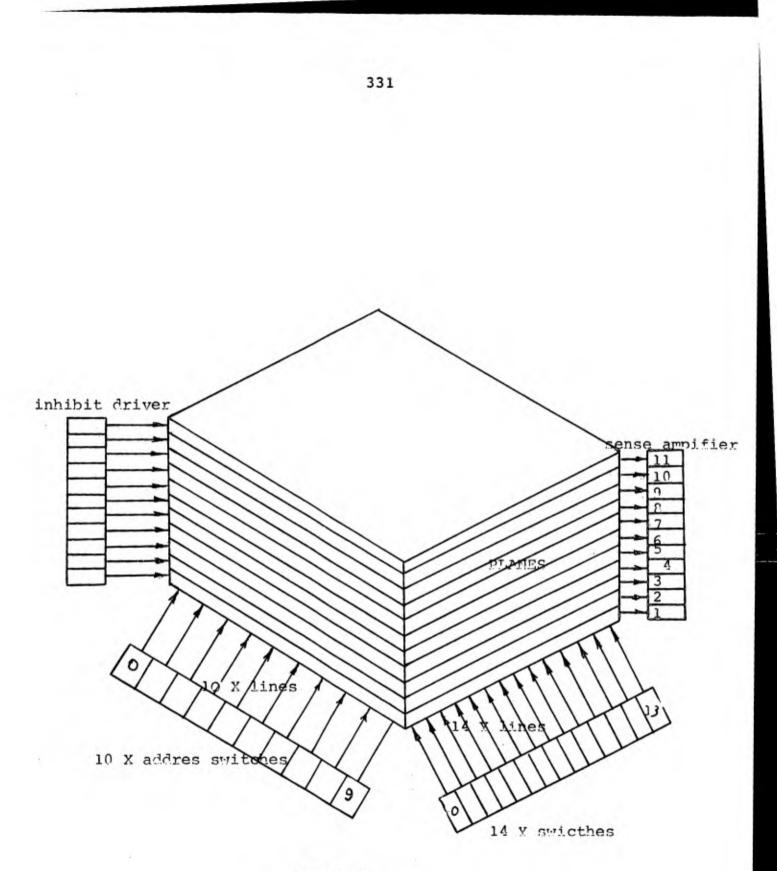
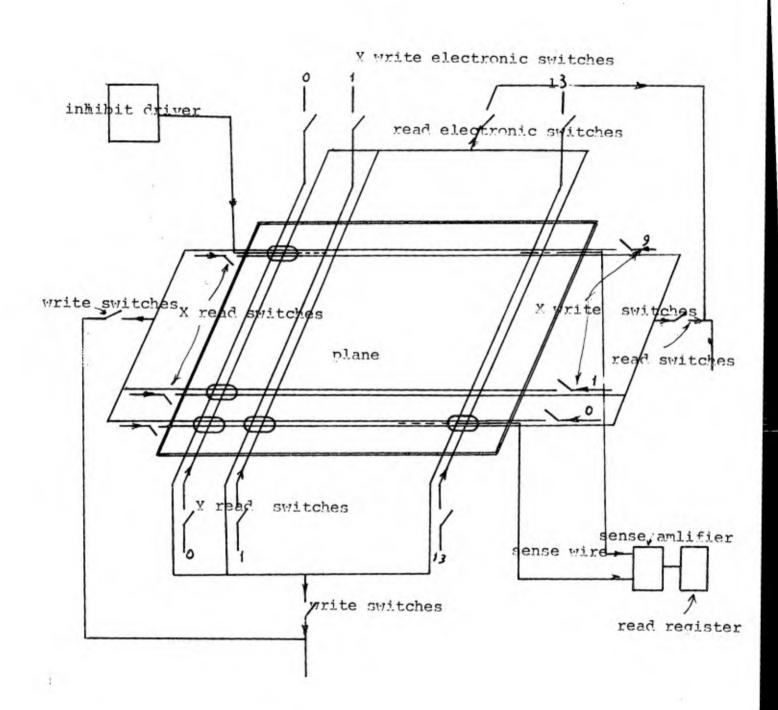


Fig 6-29



fig(6-29)

then passes through 14 (cores per row per plane) X 11 (planes); or 154 cores.

Similary, one Y drive line goes through all 10 cores in a given row of a plane, and continues through the corresponding 10 cores in each of the other planes. The Y lines crosses the X line at an angle of 90°, in each core plane. In a 10 x 14 core array, one Y drive line then passes through 10 (cores per row per plane) x 11 (planes), or 110 cores.

An inhibit drive line is associated with each bit plane.

A sense line is threaded parallel to the Y drive line through all cores in each core plane. Each core plane has one sense line. It is crossed over at the middle of the core plane to cancel the effect of the mutual coupling between it and the Y drive line.

The logic flow from and to main core storage, is shown in figure (6-30) The sense lines from core storage must pass through sense amplifiers. The induced voltage pulse resulting from the readout of a logical 1 is of such low amplitude (40 to 50 miltivolts) that it must be amplified to set the associated read register.

To select the induced voltage pulses from noise on the sense line, a short pulse (strobe) gates the sense amplifier just when the ferrite core is being read out.

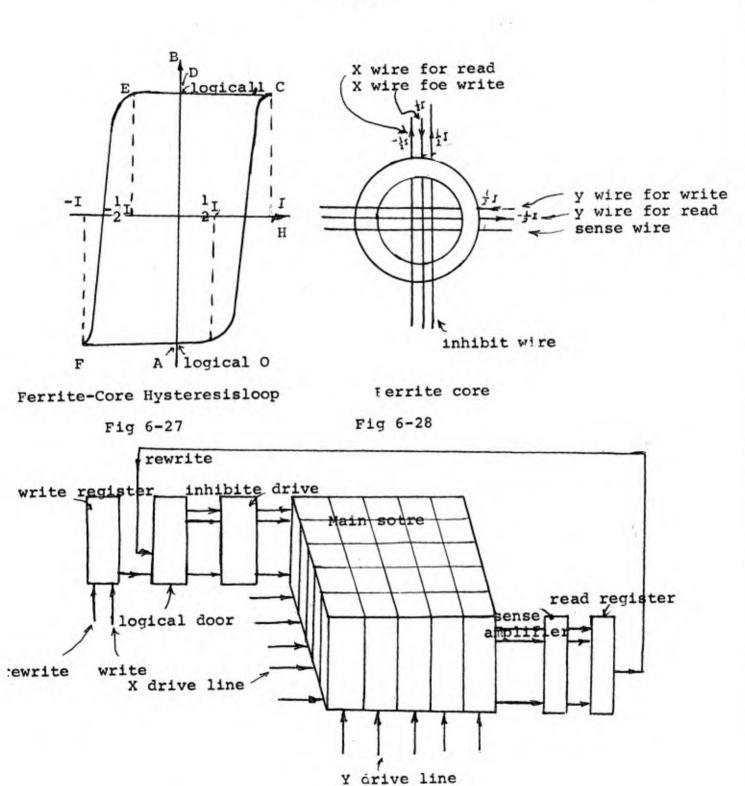


Fig 6-30

The read register holds the information read from storage. This information can be written back into the cores by the path indicated.

The inhibit drivers control the writing into cores. Because write current is in a direction opposite to read current, all cores would be set to 1 if the inhibit drives were not activated during write time. The inhibit drivers prevent writing a one in a core by allowing a half current to flow in the inhibit line that opposes the write current in the X drive line.

The core storage cycle (11.5 microseconds) is divided into two parts: The read portion and the write portion. The read portion of the cycle makes available to the system information contained in the core storage unit. The write portion of the cycle allows information contained in other unit to be entered into the core storage.

These operations of memory are explained in some words as following:

<u>Read:</u> On a read portion of storage cycle, the same relative core in each core plane is impulsed with X and Y drive current in the read direction. This current sets all cores at a particular address to O. Any ones present during read time are picked up on the sense lines, amplified by the sense amplifiers, and used to set the read register. Cores that contain zero have no induced voltage in the sense line; therefore, those bits in read register rest inchanged (0).

Write: During the last part of the storage cylce, the X and Y drive lines of writing are impulsed in the opposite direction or write direction. This write current is in a direction to place a 1 in the cores being addressed. However, if any of the cores are to remain zero, the corresponding inhibit gates turn on their associated inhibit drives. With the inhibit drivers on, inhibit current flows in the inhibit line in a direction opposite to the current in the X drive line. Therefore, the X drive current is cancelled and the core remain at zero. The read write signal the inhibit signals and output signal of sense amplifiers, are shown in figure (6-31).

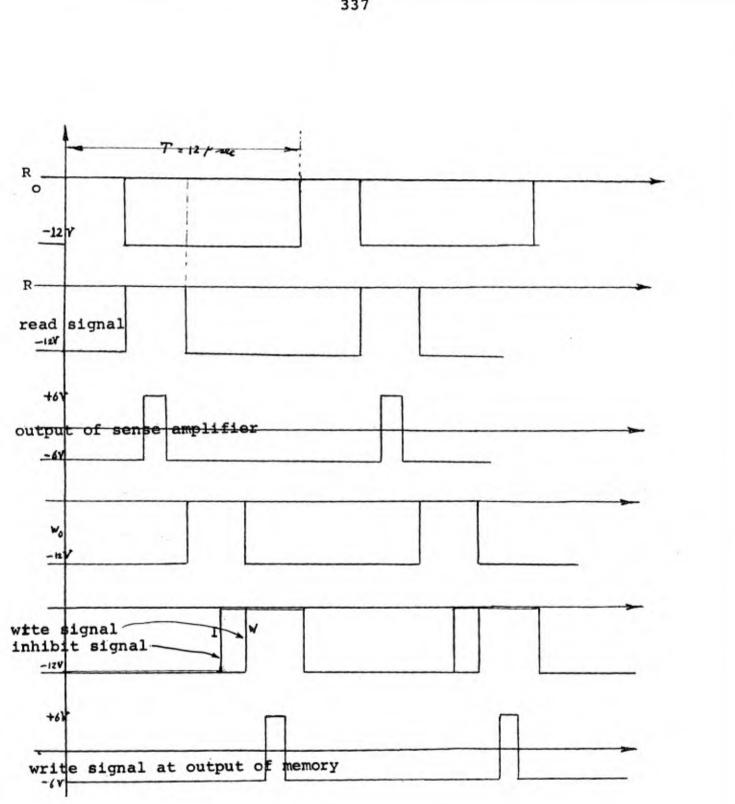


Fig 6-30

## 6.5. Conclustons.

This investigation has led us to develop a new method of incremental computation, with multiple increments, which has the advantage of speed, versatility, flexability and short slewing time for jump function over the unitary incremental computation ( $D_o D_o A_o$ ).

A new effective method has presented for the computation and comparison of various errors in unitary and multiple incremental computation. This was necessary in order to choose the algorithms of the machine in the most economical and convenient way with the desired accuracy in results. It was shown that by choosing more accurate interpolation formula for incremental computation, it is possible to reduce the error of method as low as possible. But from calculation of this error, it was shown that, it is not worthwhile to use higher than first degree interpolation formula ( trapezoidal ) for unitary increment computation. It was also concluded that, the second degree interpolation formula ( three points method ) is a good approximation for multiple increment computation.

From the computation of quantization error, it was seen that, the quantization error is same for each method of integration but it depends to the quantums  $\Delta x$ ,  $\Delta y$  and the number of bits h in multiple increments. The study of this error conducts us to choose the register's length and the speed of the computers. The study of round off and transmission errors showed the way of minimizing them in incremental computers.

The calculation of the total error enabled us to compare the error for different methods of integration in unitary and multiple increment computation. From the comparison of these error, we deduced the choice of algorithms for unitary and amultiple increment computer, with the desired accuracy.

We explained the design, algorithm and development of a new type of transistor incremental computer which is desvised by the author in the Laboratory of Brussels University. This computer performs the integral operation on the basis of unitary and multiple increment computation. In addition, this machine is capable of doing the basic mathematical and other combined operations. By a new algorithm, the interconnection between the integrators was realized on the patch panel only by one lead. A new method of multiplication of two functions is represented which use only one integrator with higher accuracy instead of three integrators which were used before.

The memories of computer can be used for incremental  $_{\rm OT}$  numerical operation of computer or works as the independent memory device. The selector of control unit gives the order of integration sequence to the arithmetic unit in only desired sequences. The time of computing the integration in general purpose is 50 msec. (with clock frequency of 1 MHz ) in unitary increment computer is 40 µsec. (with clock frequency of 500 KHz ), so the speed of this machine is 2.500 higher than general purpose. Still the multiple increment computation increase the speed of integration by 2<sup>4</sup> ( with same clock frequency ),

so the speed of integration is increased by  $2.500 \circ 2^4 = 40.000$  compared to general purpose digital computer.

From above discussion, it appears that future investigation would be directed to the application of incremental computation in the Direct Digital Control ( D.D.C. ), optimization and simulation of modern automatic control which requires high dynamic quality, accuracy, flexibility, speed and reliability.

## Bibliography.

I. Electronic computers and logical systems.

- J. FLORINE, Automatismes à sequences et commandes numériques.
   Bibliothèque de l'automaticien DUNOD. Paris, 1969.
- J. FLORINE, La synthèse des machines logiques et son automatisation. Presses acadèmiques européennes. Bruxelles, 1964.
- 3. J. FLORINE, Introduction à la recherche optimale de fonctions logiques. Revue A, No. 2. 1964, pp. 76-82.
- 4. S.P. FRANKEL, The logical design of a simple general purpose computer Trans., I.R.E. on electronic computer. Vol. EC.6. No.1 pp. 5-14. March 1957.
- 5. G.A. MALEY & J.E. ARLE, The logic design of transistor digital computers. Englewood cliffs, N.J. 1963. Prentice-Hall, INC, London.
- D. MAC CRAKEN, Digital computer programming. John Wiley & sons. New York. 1957.
- 7. M. PHISTER. Logical design of digital computers. John Wiley
  & sons. New York. 1956.
- M.V. WILKES. Automatic digital computers. John Wiley & sons. New York. 1956.

II. Incremental computers.

- BROWN, E.L. Digital Computer Design. Academic Press. New York.
   1963.
- BRIT, J. A directly-coupled serial adder, designed for use in D.D.A. I.R.E. Vol. 23. April 1962.
- 3. DONAN, J.F. The serial memory D.D.A. math tables aides to computation. No. 38, pp. 102-112. April 1952.
- 4. DICKINSON, M.M. A comparaison of D.D.A. and G.P.C. in guidance systems. A.I.E.E. January 1961.
- 5. GEAR, C.W. The numerical integration of ordinary differential equations, math tables aides to computation, pp. 146. April 1967.
- 6. WILF, HERBERT. S. An open formula for the numerical integration of first order differential equation. Math Tables Aides to Computation. January 1958.
- 7. HILLS F.B. A study of incremental computation by difference equations. M.I.T. Thesis 1958. Cambridge Massachusetts.
- 8. HAMER, E. and PALMER, W. Comparaison of computational speeds of D.D.A. and G.P.C. I.E.E.E. Trans. Vol. EC-13. June 1964. pp. 307-308.
- 9. KNUDSEN, H.K. The scaling of D.D.A. I.E.E.E. Trans. Vol. EC.14. pp. 383-390. August 1965.

- 10. LAMSON, R.C. A division Algorithm for D.D.A. I.E.E.E. Trans. Vol. EC.13. pp. 54-55. February 1964.
- 11. LAMB, D. Design and application of D.D.A. Proceeding of I.R.E. Australia. April 1961.
- 12. MAYOROV, F.V. Electronic Digital integrating Computer. Iliffe Books. London 1964.
- 13. MONROE, A.J. Digital Process for sampled data systems. John Wiley & Sons. New York 1962.
- 14. PALEVSKY and J.F. HONWELL. Digital differential equation solner. Instr. and Control system. Vol. 63. pp. 118-121. April 1963.
- 15. RAYMOND, F.H. Vice président de l'A.I.C.A. Réflexions sur les machines à calculer, A.I.C.A. Février 1966.
- 16, RAWLEY, G.C. Transistor Circuits for D.D.A. Proc. I.E.E.E. Vol 106, Part, B. suppl. No 16, pp. 685-698, May 1959.
- 17. REARILH, Function generator of digital differential Analyzer. I.R.E. Trans. Telemetry and remote control. Vol TRC-1. No3 pp. 8-12. August 1955.
- 18. SHABELUND, D. The numerial process of a binary D.D.A. University of Utah. Salt Lake City 1, Utah. U.S.A. August 1953.
- 19. STOLLER L, MORRESON. D. A method for numerical integration of differental equation. Math Table. Aides to computation. October 1958.

- 20. SHILEIKO, A.V. Digital differential Analyzers Peramon Press. Oxford 1964.
- 21. TURTLE, O.C. Incremental Computer error Analysis. I.E.E.E. Trans. on Commun and electronic. Vol. 82. pp.422-425. September 1963.
- 22. TOOTHL,G.C. The incremental digital Computer, first principals of operation and use Process control and Automation. September 1958.
- 23. SPRAGUE, R.F. Fundamental Concepts of D.D.A. Math Tables. Aides to computation. Vol. 6. No. 37. pp. 41-49. January 1952.
- 24. HANGE ,O.error analysis of D.D.A internal raport T.H.Achen Germany. September 1960.

- III. Automatic Control:
- BERTAN. The effect of quantization in sampled data feedback system. A.I.E.E. pp. 177. September 1958.
- BELLMAN, R. KABABA, R. Dynamic Programming and feedback control.
   1St IFAC CONGRESS. pp. 460-464.
- 3. CURRY, E.E. The analysis of round off and truncation error in a Hybrid control systems. I.E.E.E. Trans. on Automatic Control. pp. 601. October 1961. Vol AC.-12 No 5.
- HOUSEHOLDER, A.S. Principal of numerical Analysis.
   M.C. Graw-Hill. New York 1953.
- 5. FALB, A.M. Optimization Control on introduction to the theory and its application. M.C. Graw-Hill. New York. 1966.
- KORN G.A. and KORN T.N. Electronic Analog Computers.
   M.C. Graw-Hill. New York 1956.
- 7. LARSON, R.E. Optimum Quantization in Dynamic systems. I.E.E.E. Trans. on Automatic Central. Vol AC-12 No 2. pp.162. April 1967.
- 8. JOHNSON, G.W. Upper bound on Dynamic quantization error in control system via the direct method of Liaquenov. I.E.E.E. Trans. on Automatic Control. Vol AC-10 No 4. pp.432. October 1965.
- 9. JOHNSON, G.W. and G.N.T. LACK. Comment on "Upper Bound on

Dynamic quantization error in Digital Control systems via the the Direct method of Liaquenov. I.E.E.E. Trans. on Automatic Control. Vol. AC-11 No 2. pp.331. April 1966.

- MAITRA, K.K. and P.E. Sarachich. Digital compensation of continious data beedback control system. Trans. A.I.E.E. Vol. 75. Pt II. pp. 107-114. 1956
- 11. MISKLEIN & BRAUN. Adaptive Control systems. M.C. Graw-Hill.
- MONROE, A.J. Automatic Control digital processes for sampled data systems. John Wiley & Sons Inc. New York 1962.
- PERETZ,R. Analyse et Synthèse de processus Automatiques. Revue A. No 1. pp. 27-37. 1963.
- PERETZ,R. Le rôle du régulateur dans les processus industriels. Revue A. No2. pp. 86-96. Avril 1961.
- PERETZ, R. Opérateurs Hybrides. 4th International Analogue Computer Meeting.
- 16. JOSEPH, Peter D. Quantization in linear control systems. I.E.E.E. Trans. on Automatic Control. Oct. 1965, Vol AC-10, n°4, p. 439.
- PETERSON, G.P. Basic Analog Computation. University of Arizona. The Mac Millan Co., New York 1967.
- RAGAZZINI, J.R. & FRANKLIN, G.F. Sampled data control systems. McGraw-Hill, New York, 1958.
- 19. RAGAZZINI, J.R. Digital computers in feedback systems. I.R.E. Convention record. Vol.5, Pt.II, 1957, pp.33-42.

- 20. RYDER, J.D. Engineering electronics with industrial application and control. Mc Graw-Hill Company, LTD, NewYork, 1957.
- 21. SILBERBERG, M.Y. Dynamic optimization and control in computer control of industrial processes. Mc Graw-Hill, 1965.
- 22. SKLOVSKY, J. & RAGAZZINI, J.R. Analysis of errors in Sampled data feedback systems. A.I.E.E., Vol.74, New York, 1957, pp.5-7.

## IV. Electronics.

- 1. GOLDSTICK, G.H. Comparison of saturated and nonsaturated switching circuits techniques. I.R.E.Trans. Electronic Computer, June 1960.
- 2. HENBE, R.A. & WALSH, J.M. The application of transistors to computers, Proc. I.R.E., June 1958.
- 3. HUNTER, L.P. Handbook of semiconductor electronics, Mc Graw-Hill, New York, 1956.
- HURLEY, R.B. Junction Transistor electronics, John Wiley & Sons, New York, 1958.
- JOYCE & CLARKE. Transistor circuit analysis, Addison-Wesley Publishing company, Inc., London, 1963.
- 6. LECAN, C., HART, K. & DE RUYTER, C. The junction transistor as a switching device. Philips Technical Library, Reinhold Publishing Corp., New York, 1962.
- 7. STOVER, Wm.A. Integrated circuits, Texas Instruments Technical Seminar Series, 1966.
- PRESSMAN, A.I. Design of Transistor circuit for digital computers. John F. Rider Publisher Inc. New York, 1959.
- 9. SMITHS, J. Transistor Physics. Philips International Institute Publications, Eindhoven-Holland, 1966.

Chapter	5 - THE TOTAL ERROR AND THE CHOICE OF ALGORITHMS FOR	
	INCREMENTAL COMPUTERS.	219
5.1.	Total error in the integration process in unitary or	
	multiple increment computers, when the independent	
	variable of integration x, is the independent	
	variable t.	219
5.2.	Total error in the integration process in unitary	
	or multiple increment computers, when the independent	
	variable of integration x is a function of the	
	independent variable t.	229
5.3.	Relative error in the integration process in unitary of	or
	multiple increment computers.	239
5.4.	Minimization of the transmission error in unitary or	
	multiple increment computers.	245
5.5.	Choice of number of bits in multiple increment compute	r.262
5.6.	Choice of algorithms for unitary and multiple incremen	it
	computers.	272
Chapter	6 - A NEW TYPE OF UNIVERSAL INCREMENTAL COMPUTER.	
	DESIGN - DEVELOPMENT - CONSTRUCTION	277
Intro	oduction	277
6.1.	Organization of Computer.	281
6.2.	Arithmetic unit	284
6.3.	A new concept for programming unit in incremental	
	Computer.	317
6.4.	Main store of incremental Computer.	326
6.5.	Conclusions.	338
Bibli	ography	241